Модель прерываний в IA-64

IA-64 Interruption Model

- **Parallel instruction execution, . . .**
  - **Exception delivery is sequential & precise**
    - **All exceptions reported on the excepting instruction (including numeric exceptions)**
- **"Interruption" is IA-64 term for...**

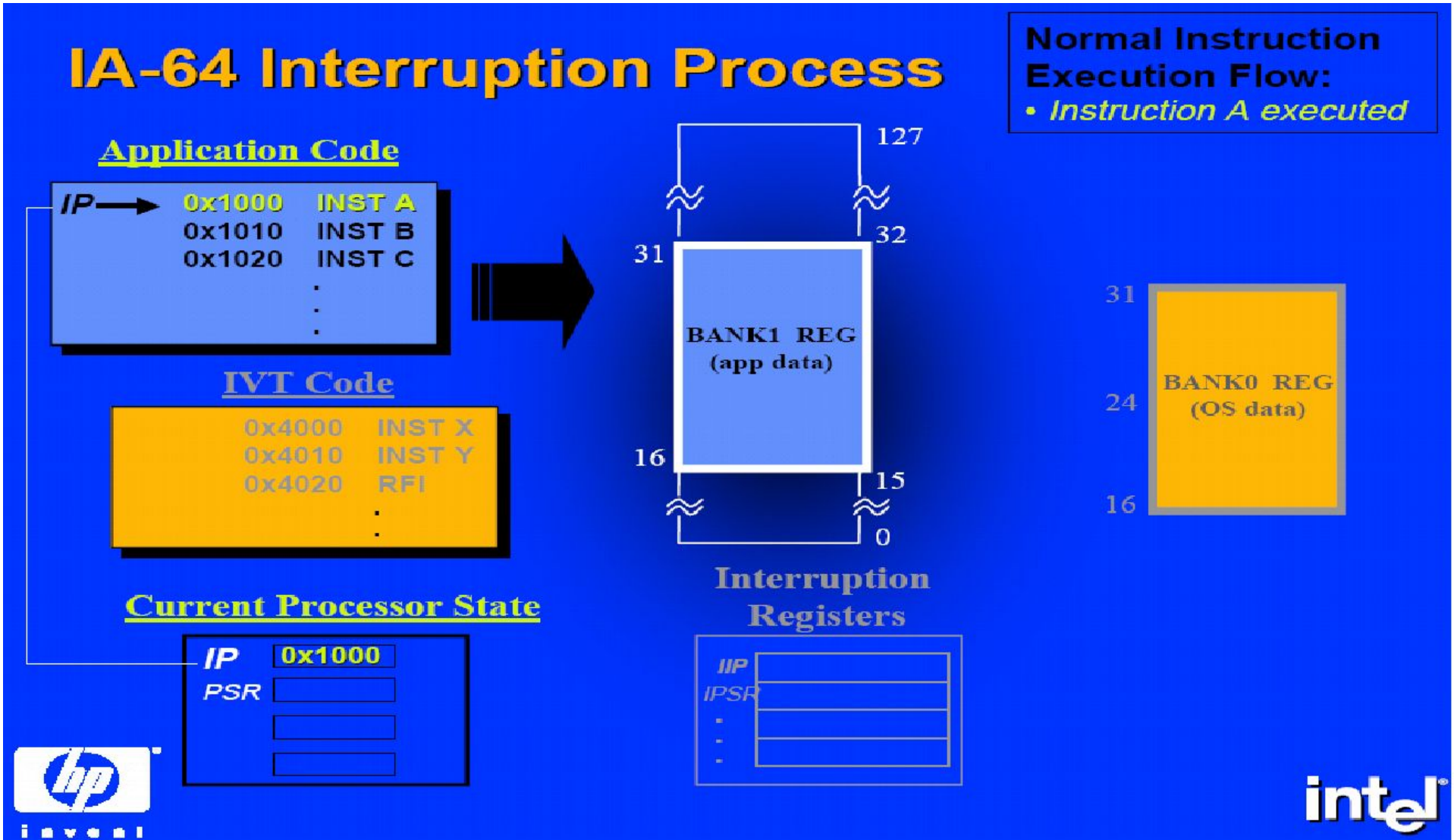
Abort	Fault	Trap	Interrupt
• Hardware reset • Machine check	Exception taken before instruction commit, e.g. TLB miss	Exception taken after instruction commit, e.g. FP trap	Asynchronous external event: • device or platform management interrupt • soft-reset

IA-64 Provides Precise Exception Model To Match Today's OS Designs

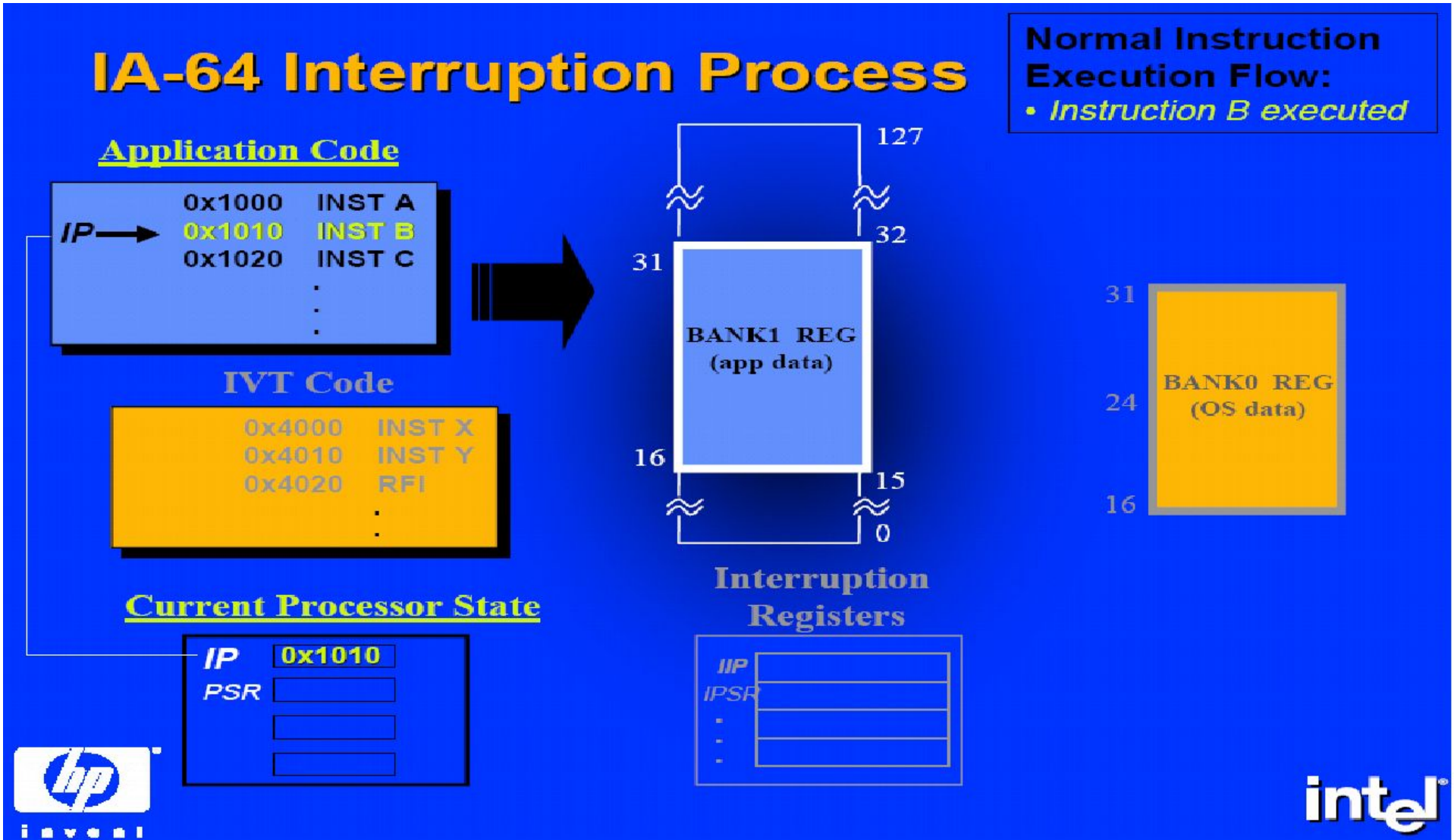
hp invent

intel

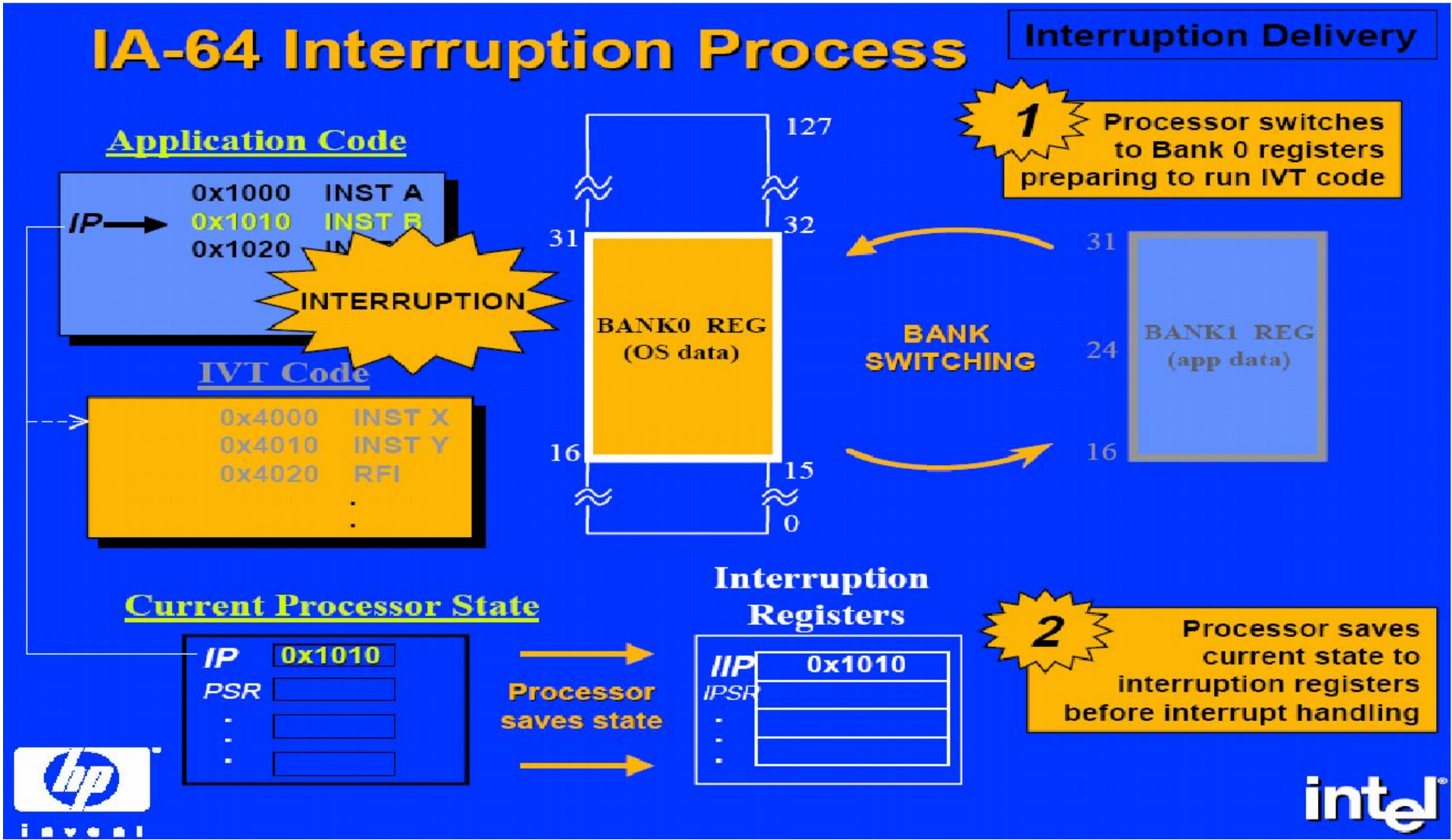
Модель прерываний в IA-64



Модель прерываний в IA-64



Модель прерываний в IA-64



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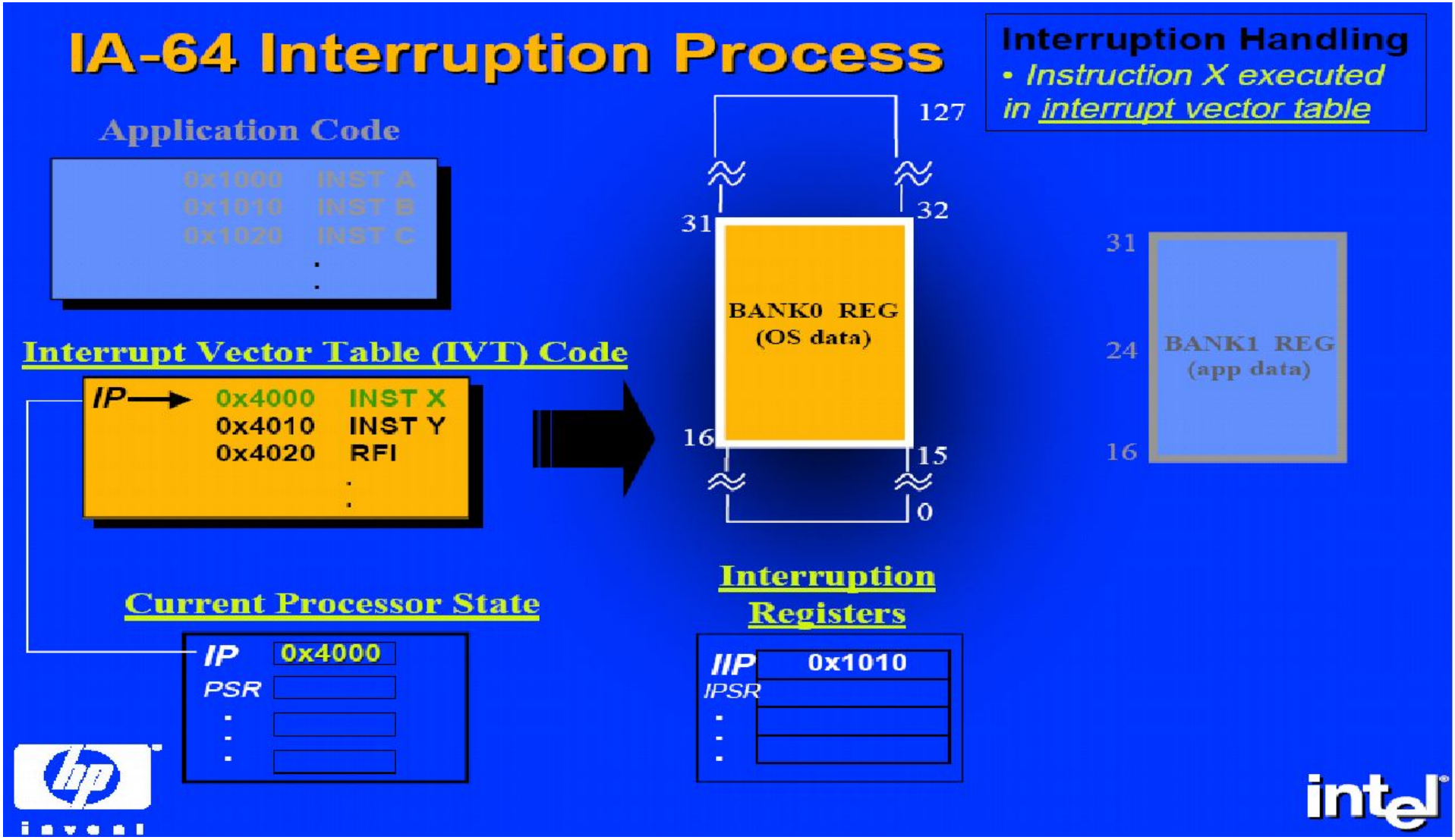

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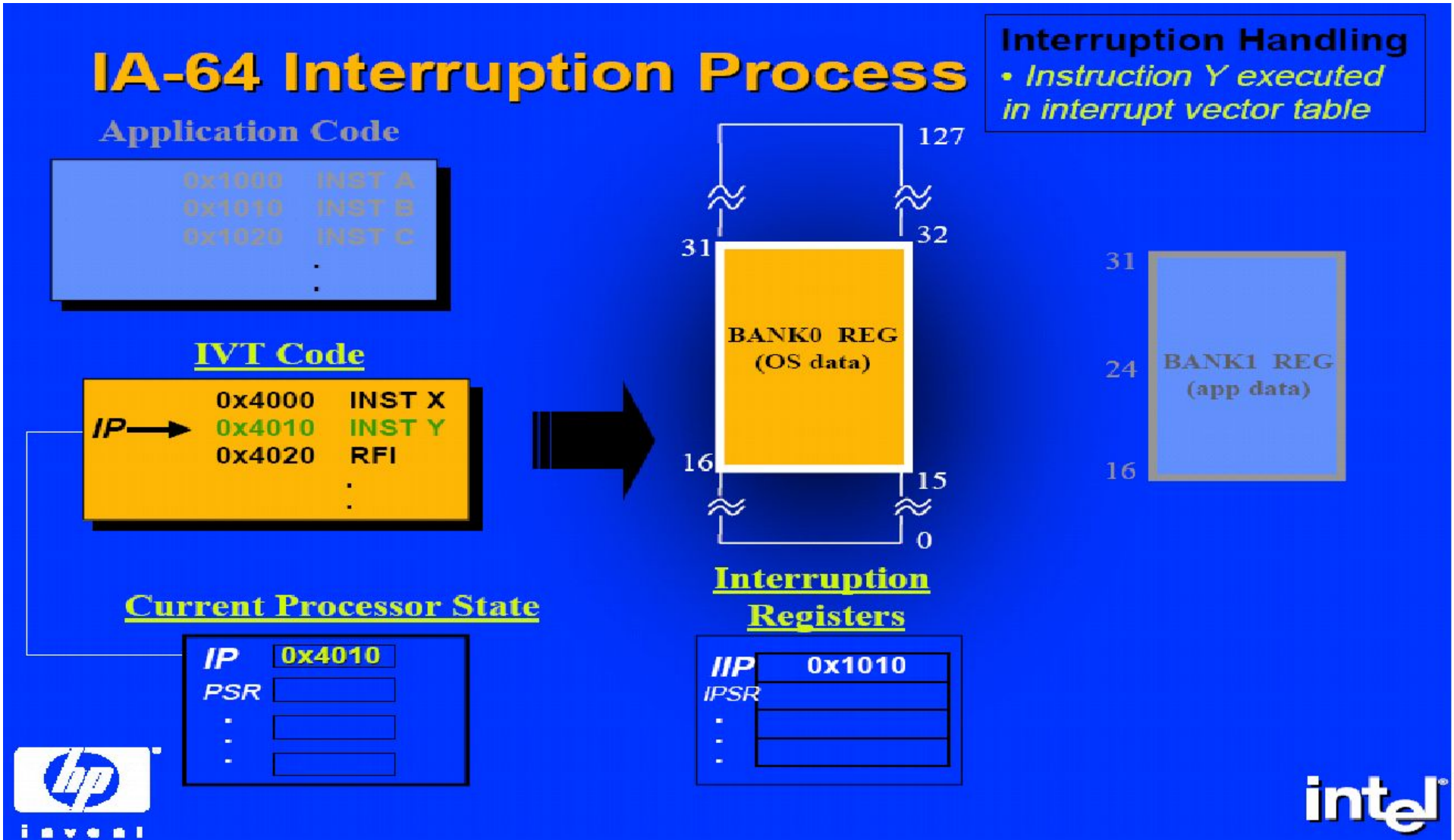

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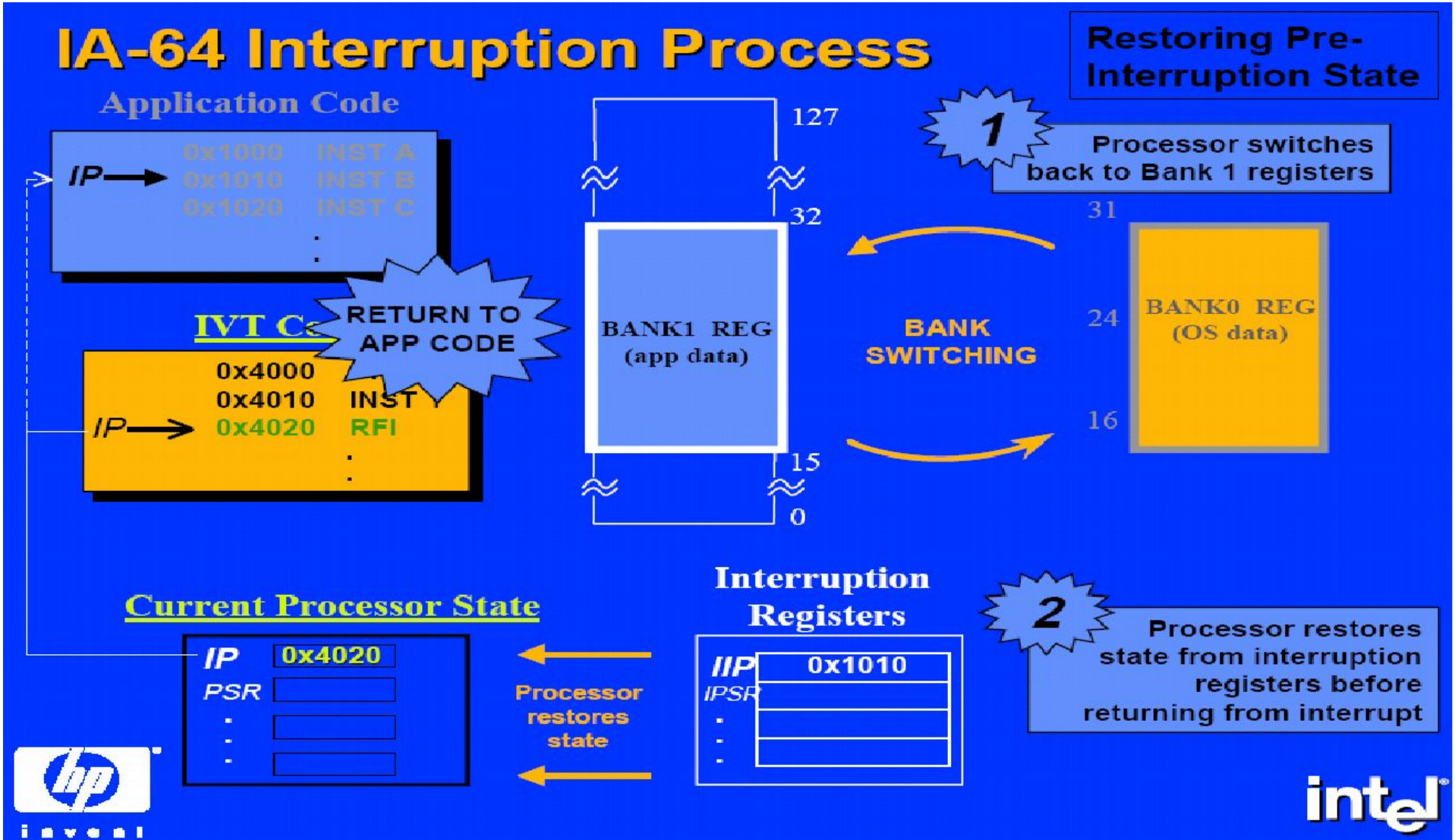

Модель прерываний в IA-64