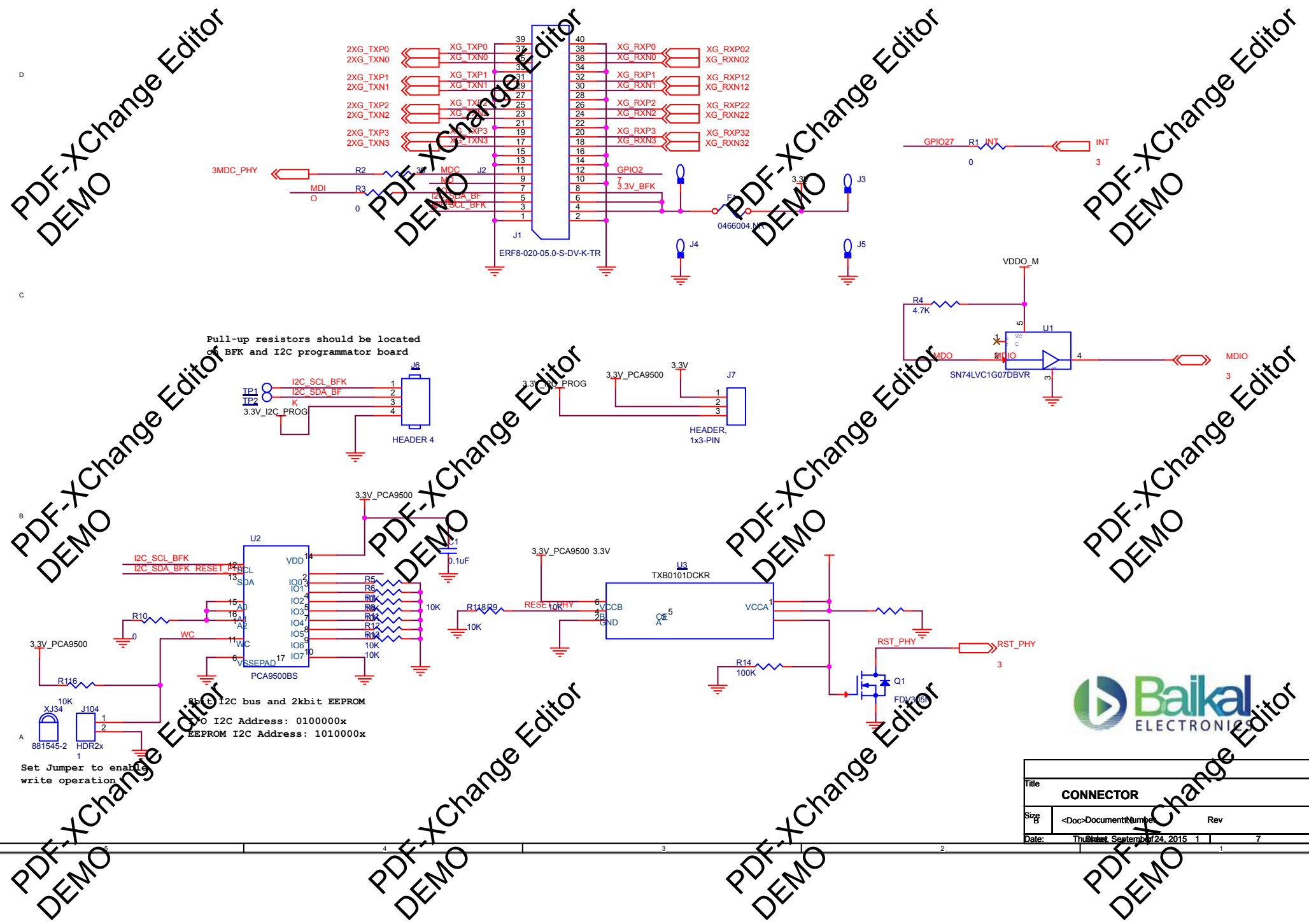
2XG_TXP0
2XG_TXN0
XG_TXP0
XG_TXN0
XG_RXP0
XG_RXN0
XG_RXP02
XG_RXN02
2XG_TXP1
2XG_TXN1
XG_TXP1
XG_TXN1
XG_RXP1
XG_RXN1
XG_RXP12
XG_RXN12
2XG_TXP2
2XG_TXN2
XG_RXP2
XG_RXN2
XG_RXP22
XG_RXN22
2XG_TXP3
2XG_TXN3
XG_RXP3
XG_RXN3
XG_RXP32
XG_RXN32
3MDC_PHY
R2
MDC
J2
MDI
R3
I2C_SDA_BF
I2C_SCL_BFK
GPIO2
3.3V_BFK
J1
ERF8-020-05.0-S-DV-K-TR
0466004.NR
J3
J4
J5
GPIO27
R1
INT
Pull-up resistors should be located
on BFK and I2C programmator board
J6
TP1
TP2
I2C_SCL_BFK
I2C_SDA_BF
3.3V_I2C_PROG
HEADER 4
3.3V_PCA9500
J7
HEADER,
1x3-PIN
VDDO_M
R4
4.7K
U1
VDO
MDIO
SN74LVC1G07DBVR
U2
I2C_SCL_BFK
I2C_SDA_BFK
RESET_PHY
SCL
SDA
VDD
C1
0.1uF
R10
WC
A0
A1
A2
VSSEPAD
PCA9500BS
R5
R6
R7
R8
R9
R11
R12
R13
R14
10K
R116
XJ34
J104
881545-2
HDR2x
Set Jumper to enable
write operation
Slot I2C bus and 2kbit EEPROM
I/O I2C Address: 0100000x
EEPROM I2C Address: 1010000x
3.3V_PCA9500 3.3V
U3
TXB0101DCKR
R118
R9
VCCB
VCCA
GND
R14
100K
Q1
FDV305N
RST_PHY
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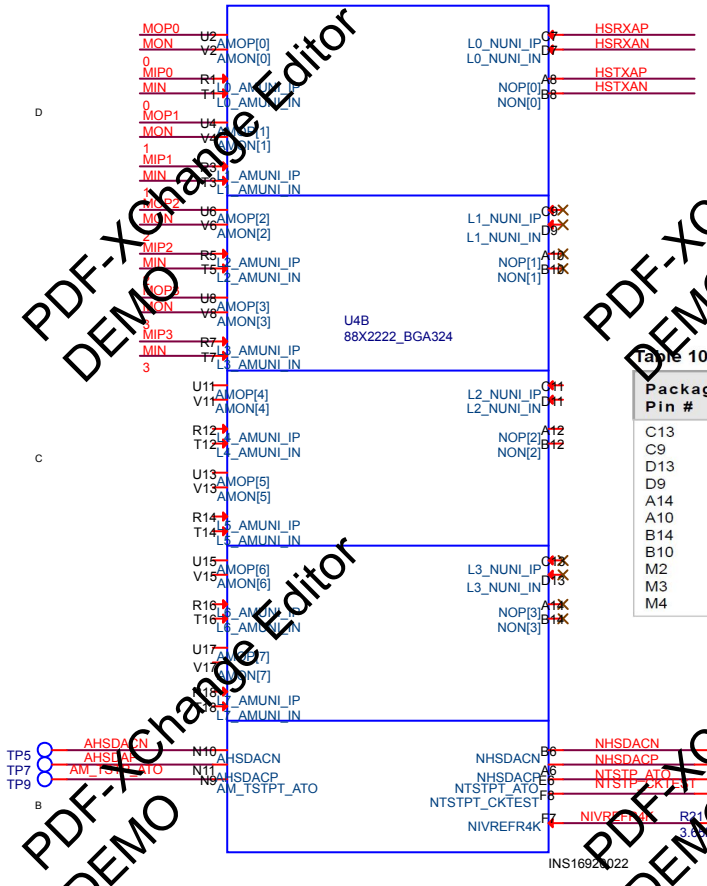
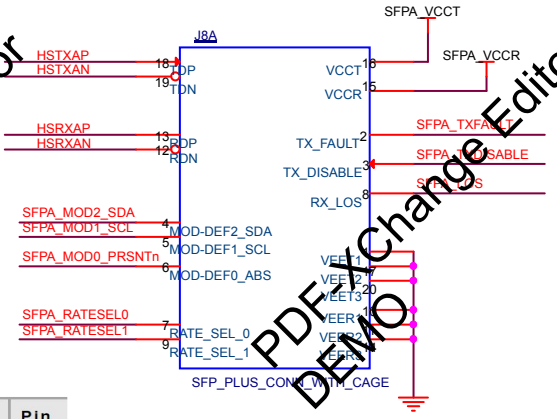
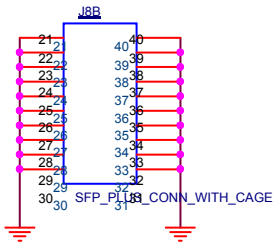
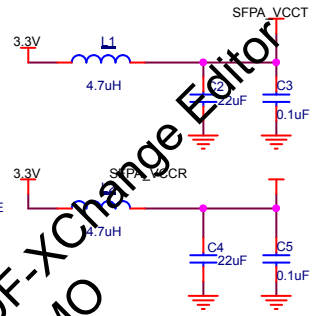


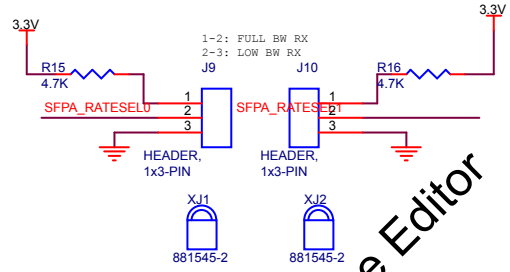
Table 10: No Connect

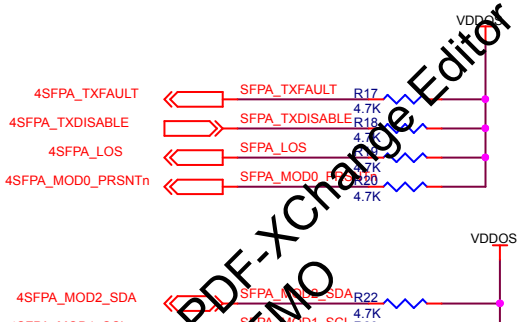
Package Pin #	Pin Name	Pin Type
C13	NOCONN	NC
C9		
D13		
D9		
A14		
A10		
B14		
B10		
M2		
M3		
M4		

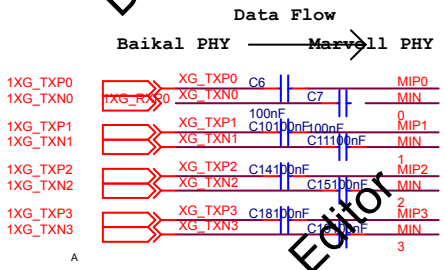


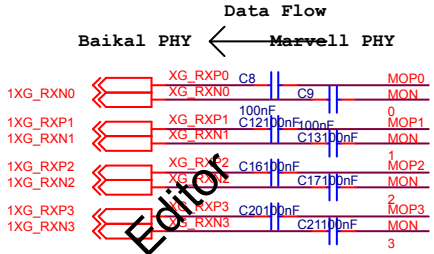












Title	SFI and KR/XAUI PORTS		
Size: B	<Doc>DocumentNumber	Rev	
Date: Friday, September 25, 2015	Sheet 2 of 7		

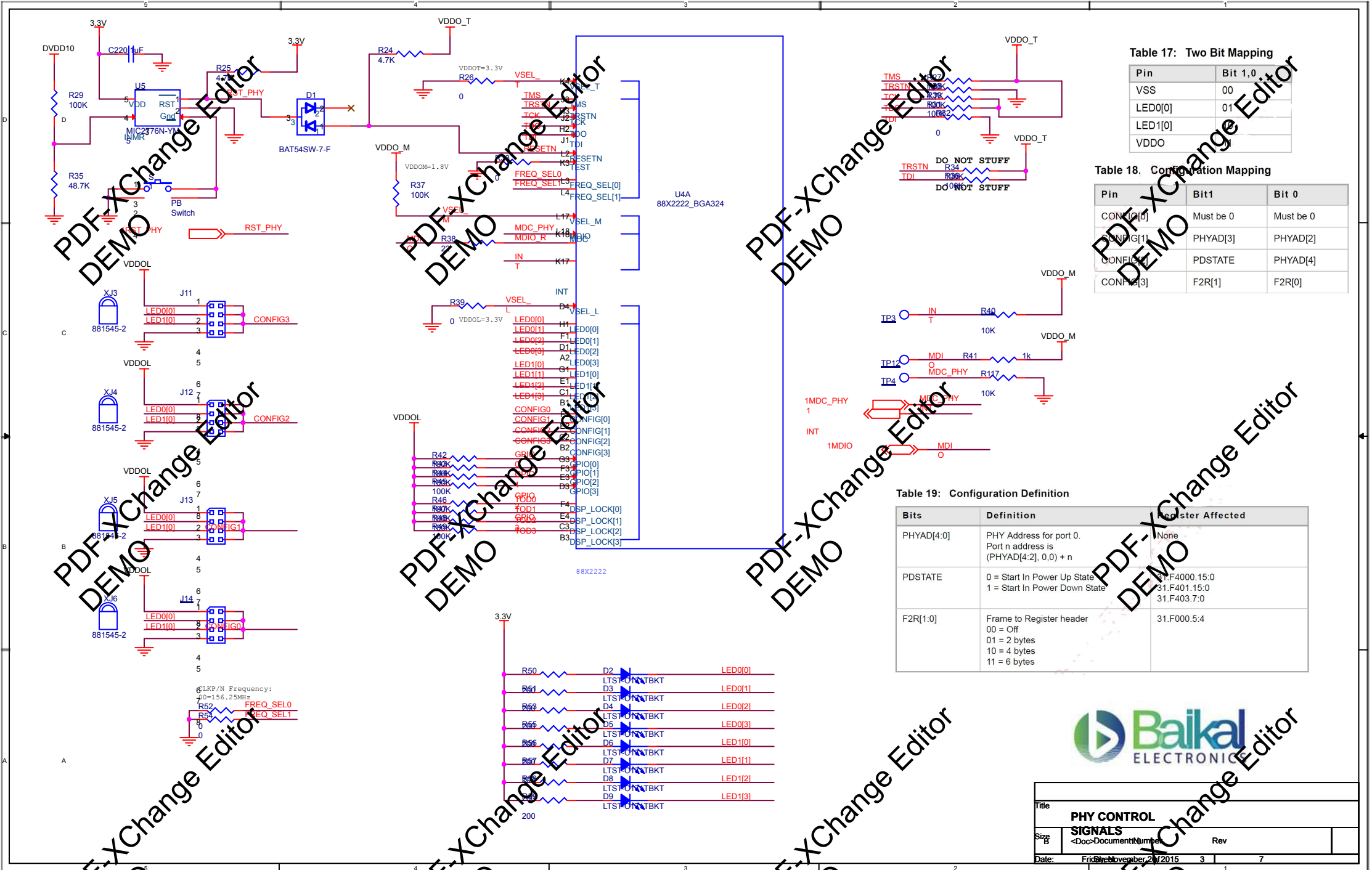


Table 17: Two Bit Mapping

Pin	Bit 1,0
VSS	00
LED0[0]	01
LED1[0]	10
VDDO	11

Table 18. Configuration Mapping

Pin	Bit1	Bit 0
CONFIG[0]	Must be 0	Must be 0
CONFIG[1]	PHYAD[3]	PHYAD[2]
CONFIG[2]	PDSTATE	PHYAD[4]
CONFIG[3]	F2R[1]	F2R[0]

Table 19: Configuration Definition

Bits	Definition	Register Affected
PHYAD[4:0]	PHY Address for port 0. Port n address is (PHYAD[4:2], 0,0) + n	None
PDSTATE	0 = Start In Power Up State 1 = Start In Power Down State	31.F4000.15:0 31.F401.15:0 31.F403.7:0
F2R[1:0]	Frame to Register header 00 = Off 01 = 2 bytes 10 = 4 bytes 11 = 6 bytes	31.F000.5:4

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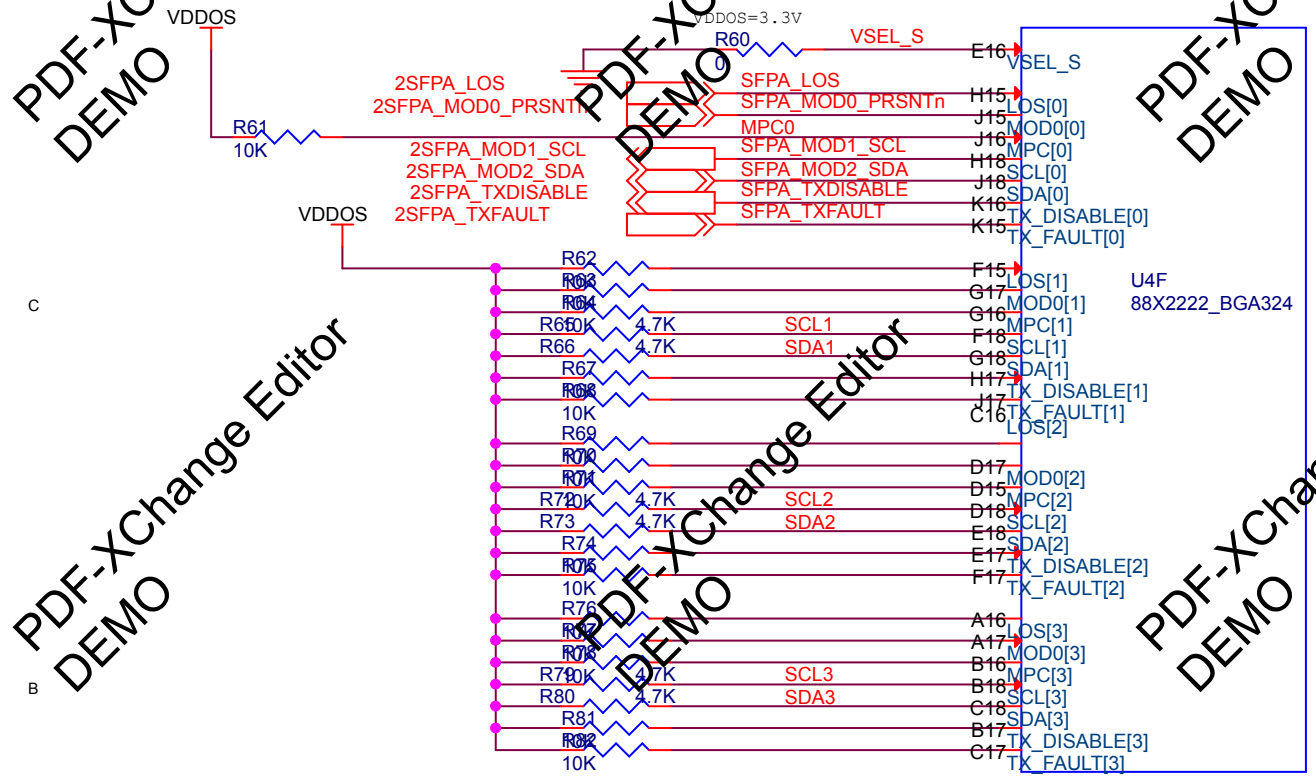

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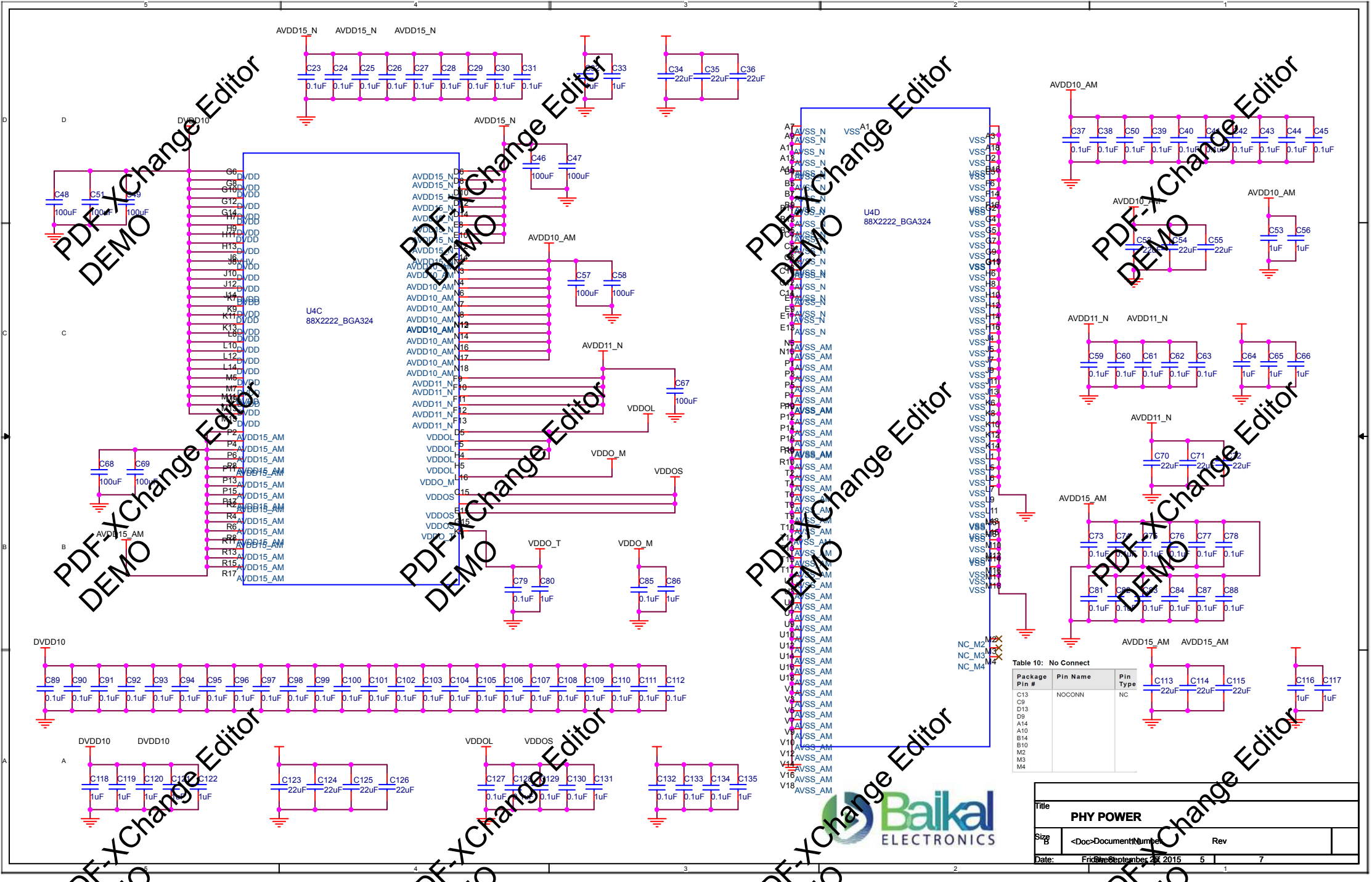
AVDD15_N
AVDD15_N
AVDD15_N
C23 C24 C25 C26 C27 C28 C29 C30 C31
0.1uF
C32 C33
1uF
C34 C35 C36
22uF
DVDD10
AVDD15_N
C46 C47
100uF
C48 C51 C49
100uF
U4C
88X2222_BGA324
AVDD10_AM
C57 C58
100uF
AVDD11_N
C67
100uF
VDDOL
VDDO_M
VDDOS
C68 C69
100uF
AVDD15_AM
VDDO_T
VDDO_M
C79 C80
0.1uF 1uF
C85 C86
0.1uF 1uF
U4D
88X2222_BGA324
AVDD10_AM
C37 C38 C50 C39 C40 C41 C42 C43 C44 C45
0.1uF
AVDD10_AM
C52 C54 C55
22uF
AVDD10_AM
C53 C56
1uF
AVDD11_N
AVDD11_N
C59 C60 C61 C62 C63
0.1uF
C64 C65 C66
1uF
AVDD11_N
C70 C71 C72
22uF
AVDD15_AM
C73 C74 C75 C76 C77 C78
0.1uF
C81 C82 C83 C84 C87 C88
0.1uF
NC_M2
NC_M3
NC_M4
Table 10: No Connect
Package Pin #
Pin Name
Pin Type
C13
C9
D13
D9
A14
A10
B14
B10
M2
M3
M4
NOCONN
NC
AVDD15_AM
AVDD15_AM
C113 C114 C115
22uF
C116 C117
1uF
DVDD10
C89 C90 C91 C92 C93 C94 C95 C96 C97 C98 C99 C100 C101 C102 C103 C104 C105 C106 C107 C108 C109 C110 C111 C112
0.1uF
DVDD10
DVDD10
C118 C119 C120 C121 C122
1uF
C123 C124 C125 C126
22uF
VDDOL
VDDOS
C127 C128 C129 C130 C131
0.1uF
C132 C133 C134 C135
0.1uF
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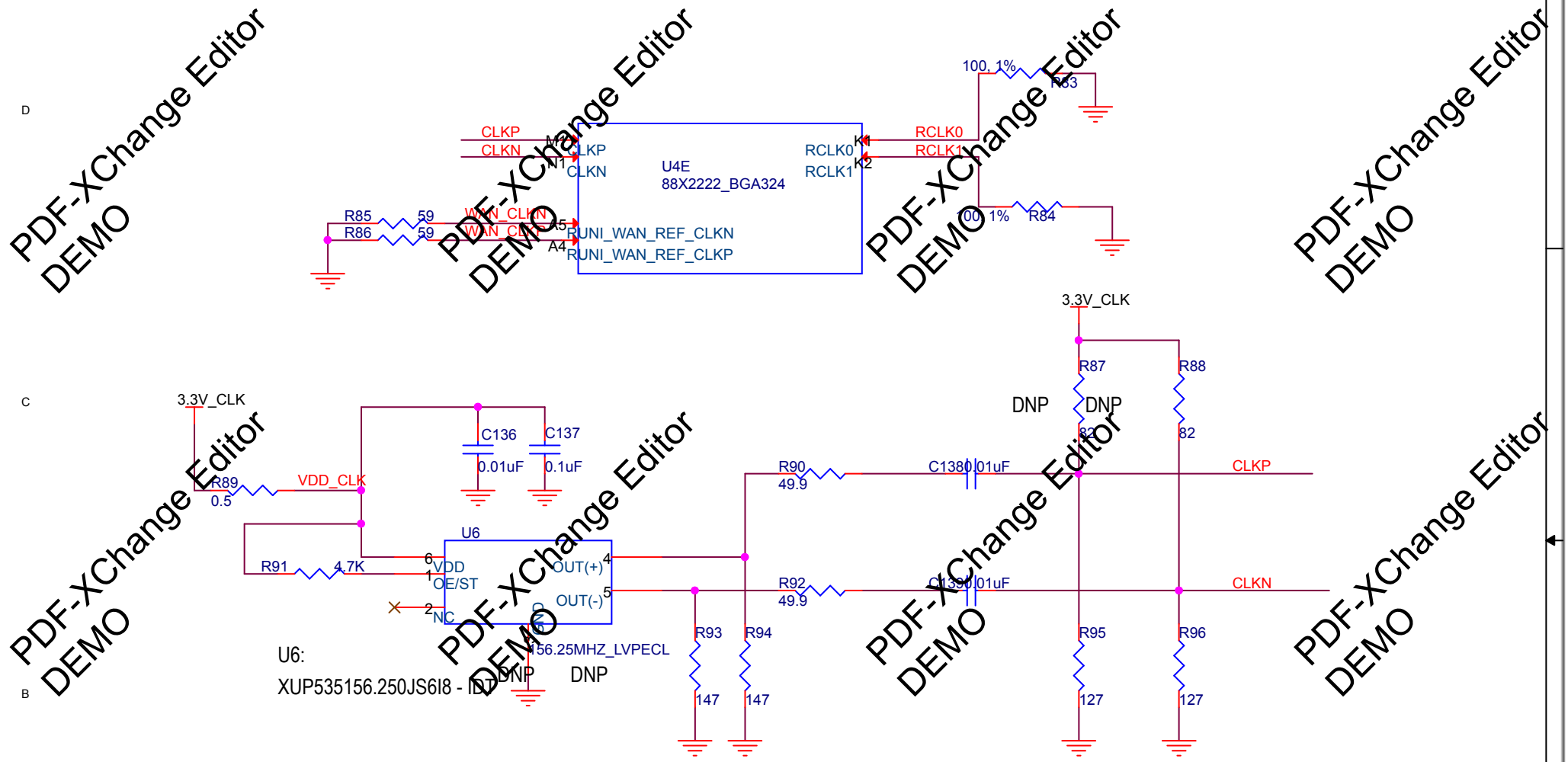

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Title: CLOCK			
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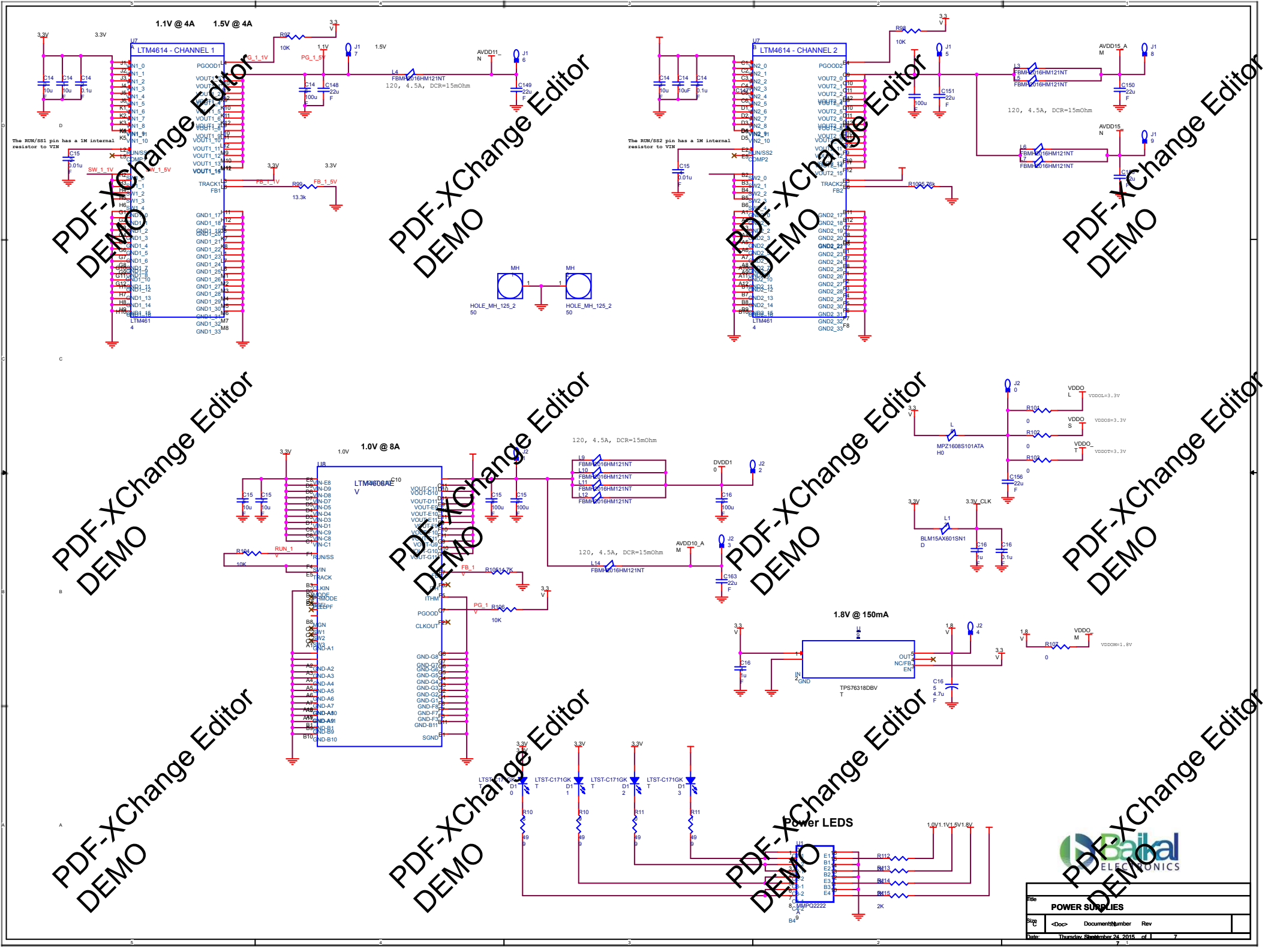
1.1V @ 4A
1.5V @ 4A
LTM4614 - CHANNEL 1
LTM4614 - CHANNEL 2
The RUN/SS1 pin has a 1M internal resistor to VIN
The RUN/SS2 pin has a 1M internal resistor to VIN
120, 4.5A, DCR=15mOhm
1.0V @ 8A
LTM4608AEV
1.8V @ 150mA
TPS76318DBVT
Power LEDS
LTST-C171GKT
MMPQ2222
POWER SUPPLIES
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