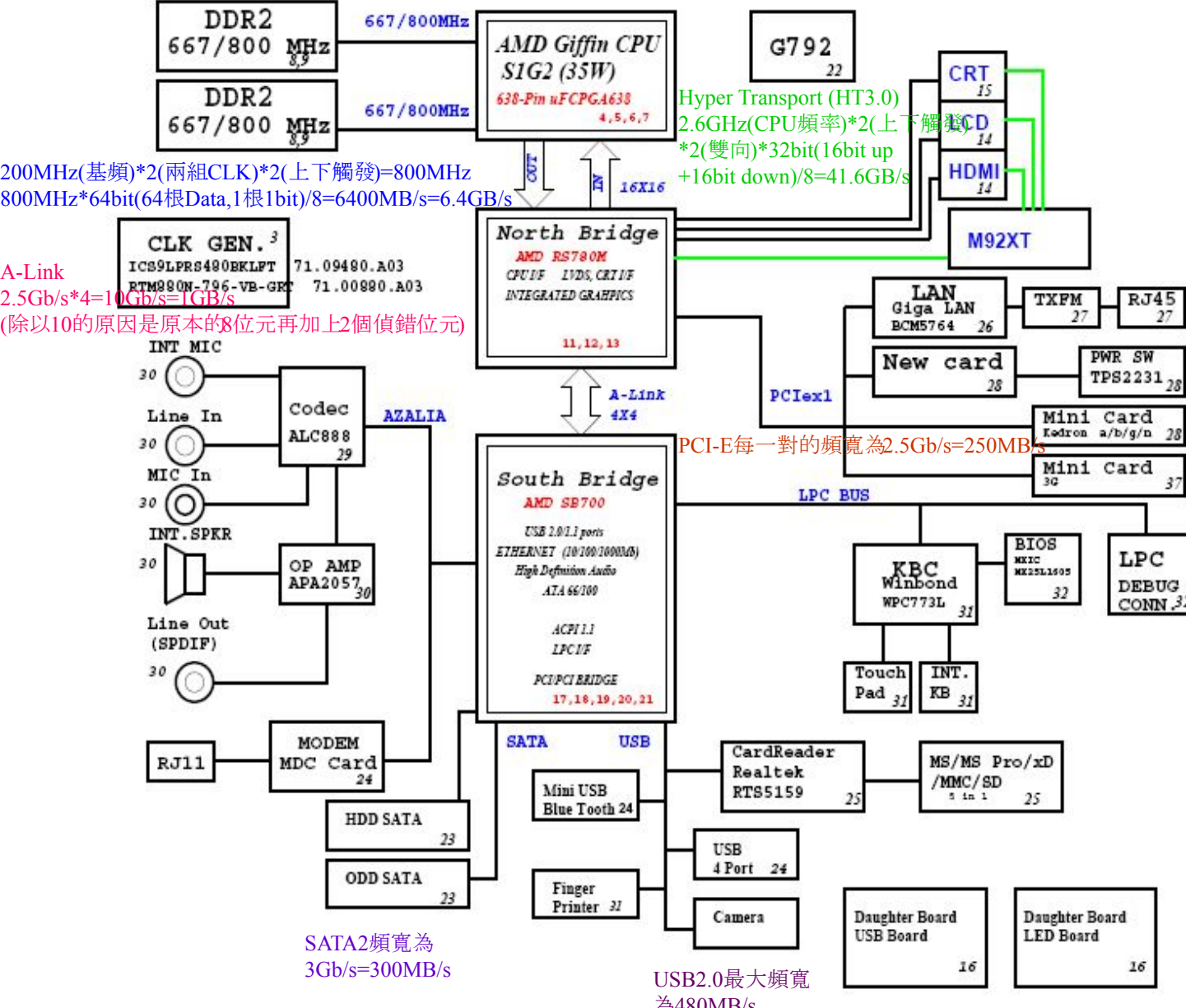


# JV50-PU Block Diagram

Project code: 91.4CH01.001  
 PCB P/N : 48.4C901.001  
 REVISION :08252- -SA

PCB STACKUP  
 TOP \_\_\_\_\_  
 VOC \_\_\_\_\_  
 S \_\_\_\_\_  
 S \_\_\_\_\_  
 GND \_\_\_\_\_  
 BOTTOM \_\_\_\_\_

SYSTEM DC/DC		TPS51125 37	
INPUTS	OUTPUTS	INPUTS	OUTPUTS
DCBATOUT	5V_05(6A)	5V_05(6A)	3.3V_05(6A)
SYSTEM DC/DC		RT8202 X 2 38	
INPUTS	OUTPUTS	INPUTS	OUTPUTS
DCBATOUT	1.81V_00(7.5A)	1.81V_00(4A)	
SYSTEM DC/DC		RT8202 39	
INPUTS	OUTPUTS	INPUTS	OUTPUTS
DCBATOUT	1.81V_02(11A)	1.81V_02(11A)	
SV_05	IDR_VRMP_02	0.65V_02	
RT9161 40		G957 40	
2D2V_00	1.81V_00(200mA)	2D2V_00	1.81V_00(1A)
G9161 40		G9161 40	
2D1V_05	1.81V_05(400mA)		
CHARGER		MAX8731 41	
INPUTS	OUTPUTS	INPUTS	OUTPUTS
DCBATOUT	CHG_PWR 1.8V 6.0A	UP+5V 5V 100mA	
CPU DC/DC		ISL6265HR 36	
INPUTS	OUTPUTS	INPUTS	OUTPUTS
DCBATOUT	VCC_CORE_00_0 0-1.55V 18A	VCC_CORE_00_1 0-1.55V 18A	VDDNB 0-1.55V 18A



Hyper Transport (HT3.0)  
 2.6GHz(CPU頻率)\*2(上下觸發)  
 \*2(雙向)\*32bit(16bit up  
 +16bit down)/8=41.6GB/s

PCI-E每一對的頻寬為2.5Gb/s=250MB/s

200MHz(基頻)\*2(兩組CLK)\*2(上下觸發)=800MHz  
 800MHz\*64bit(64根Data,1根1bit)/8=6400MB/s=6.4GB/s

A-Link  
 2.5Gb/s\*4=10Gb/s=1GB/s  
 (除以10的原因是原本的8位元再加上2個偵錯位元)

SATA2頻寬為  
 3Gb/s=300MB/s

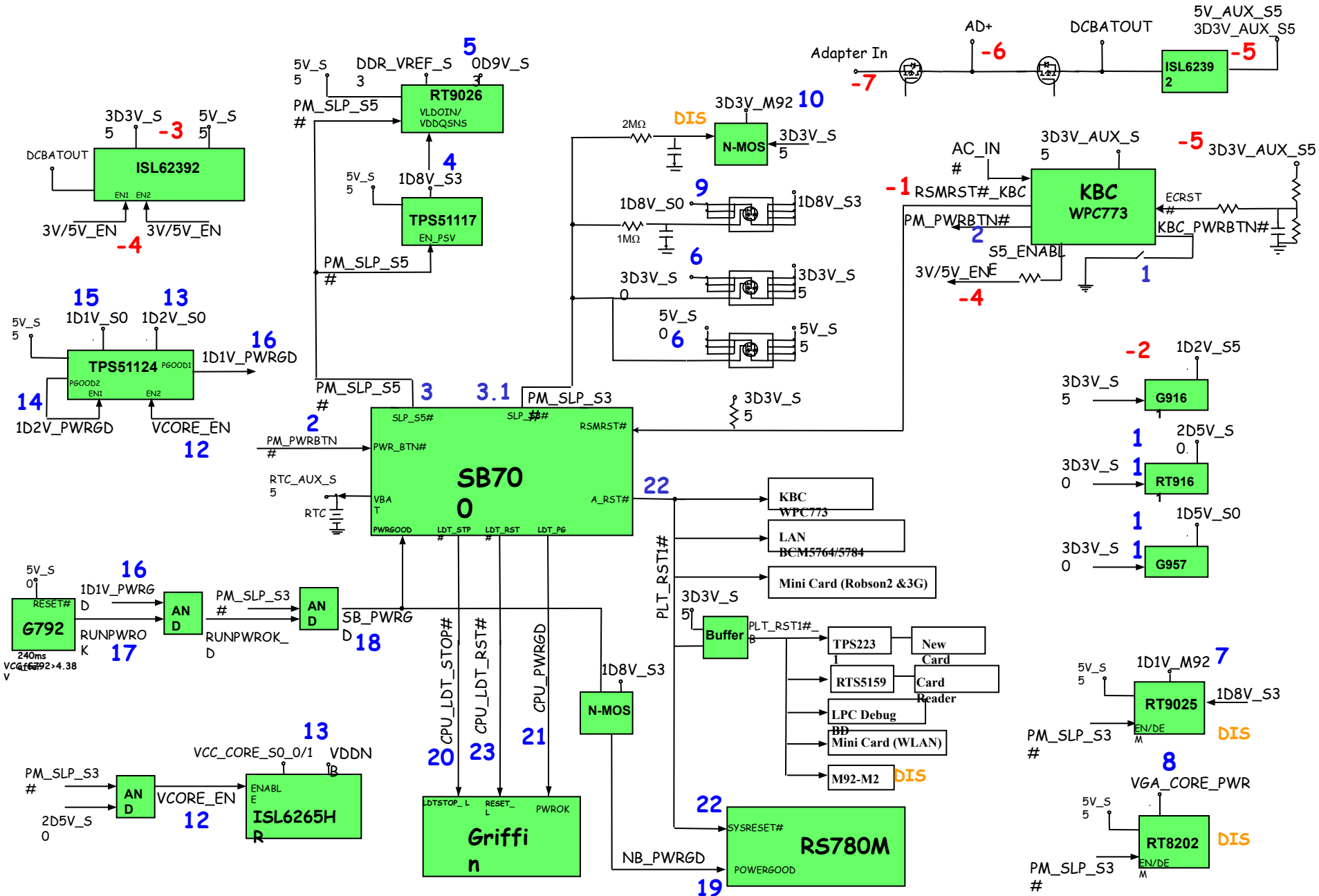
USB2.0最大頻寬  
 為480MB/s

緯創資通 Wistron Corporation  
 217, 9th, Sec.1, Hsin Tai Wu Rd., Hsinshu, Taipei 10416, Taiwan, R.O.C.

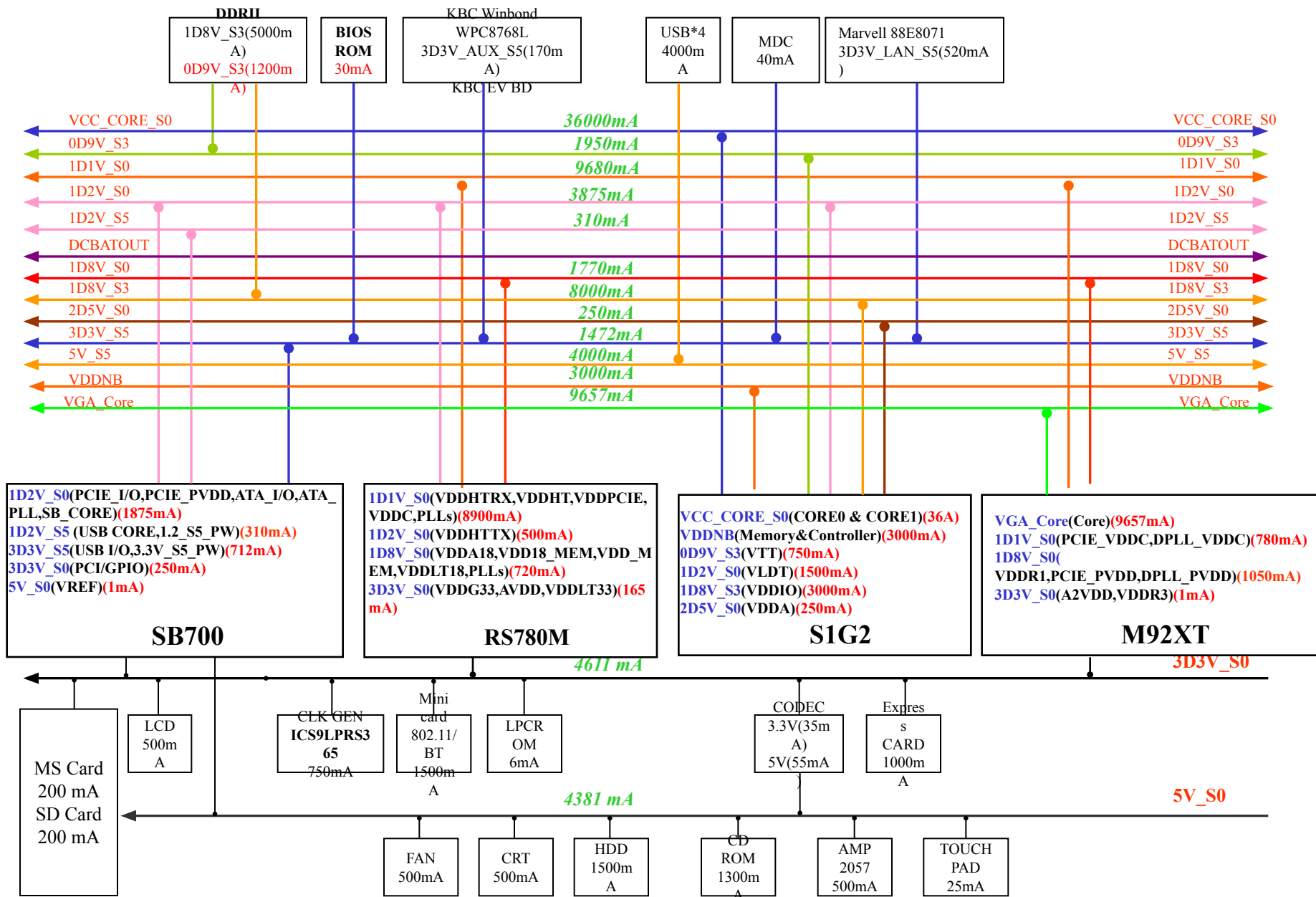
**BLOCK DIAGRAM**

Doc No: JV50-PU  
 Date: Monday, October 20, 2008

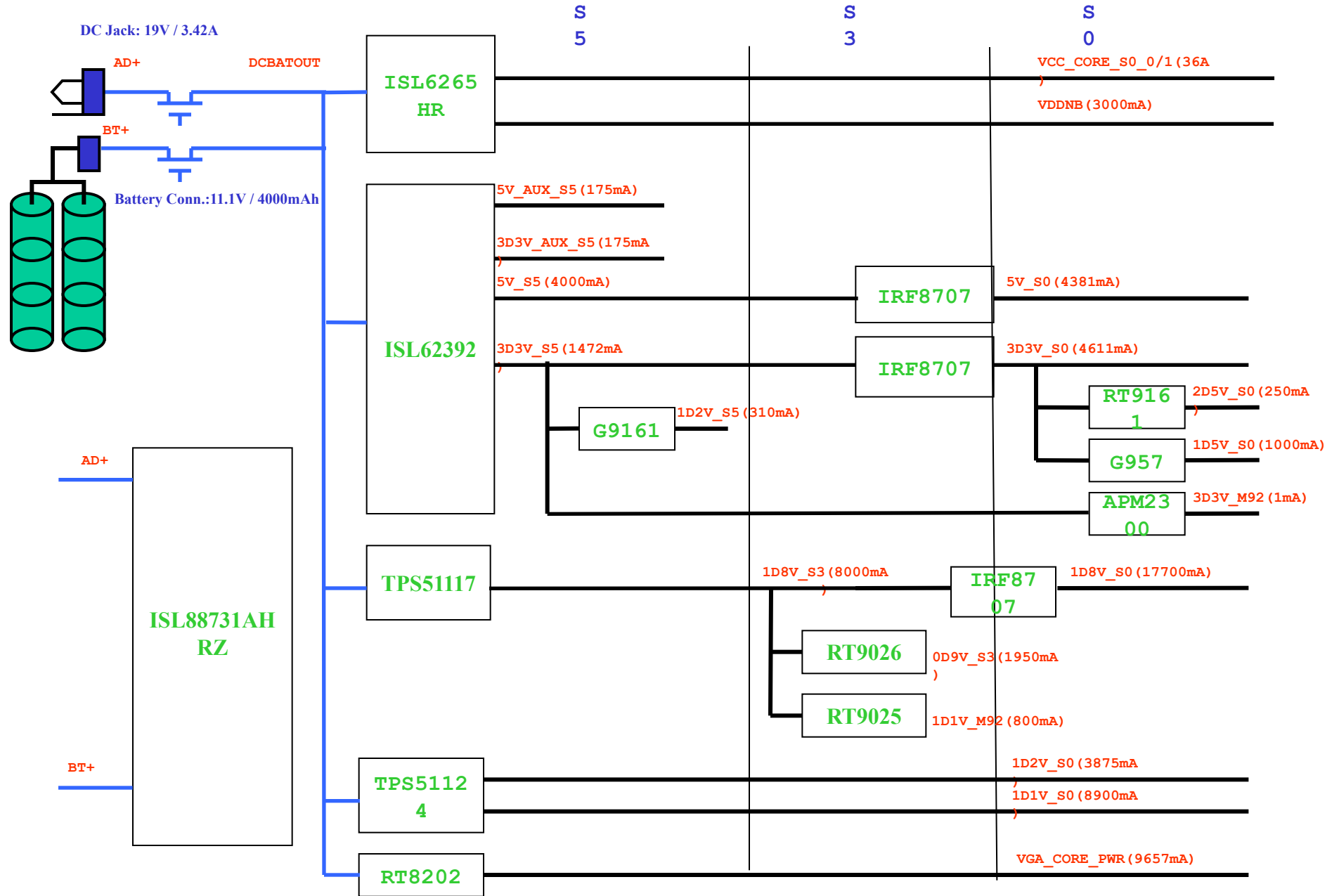
# JV50-PU Power ON/RESET Sequence



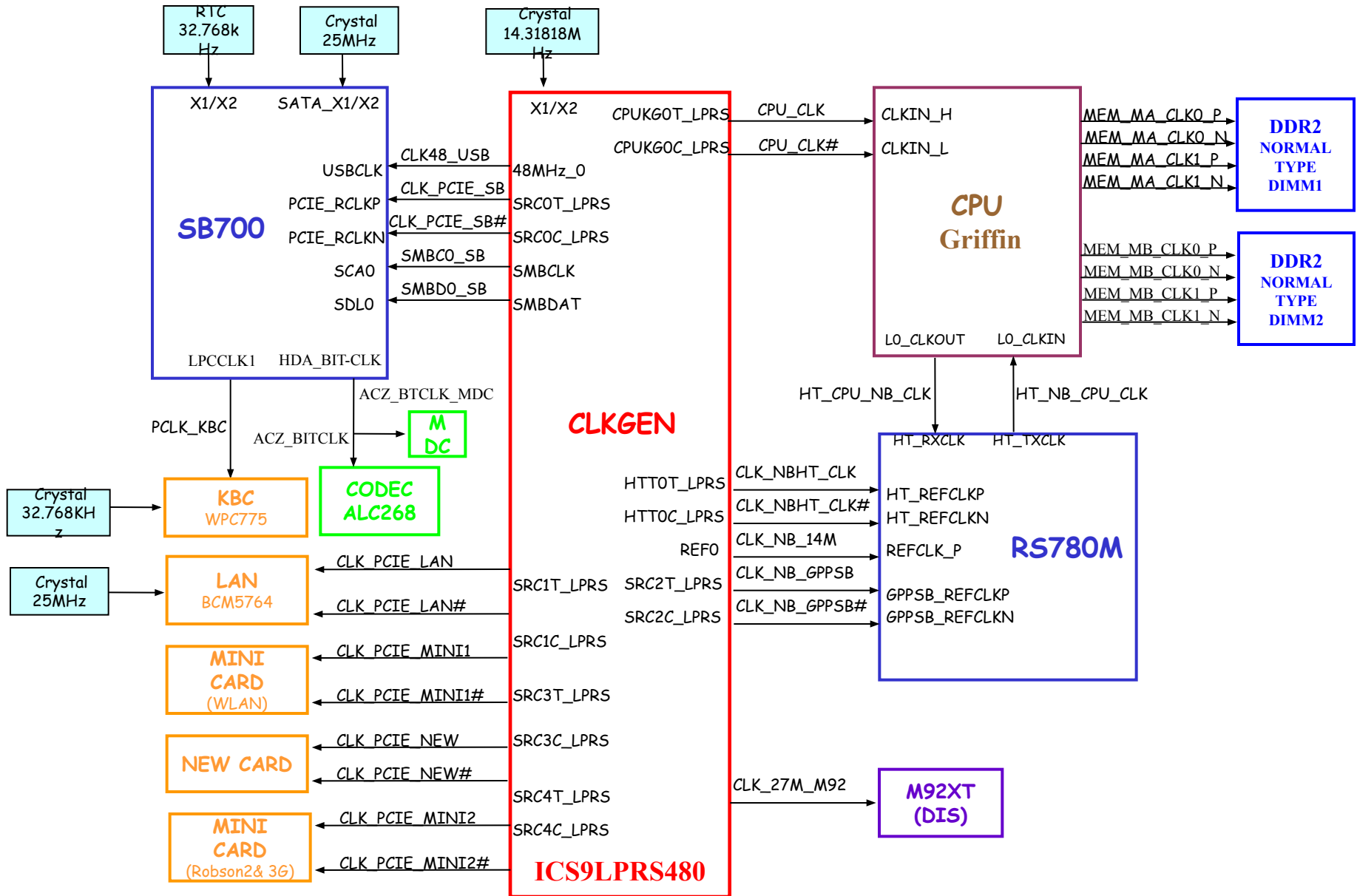
# JV50-PU Power Budget



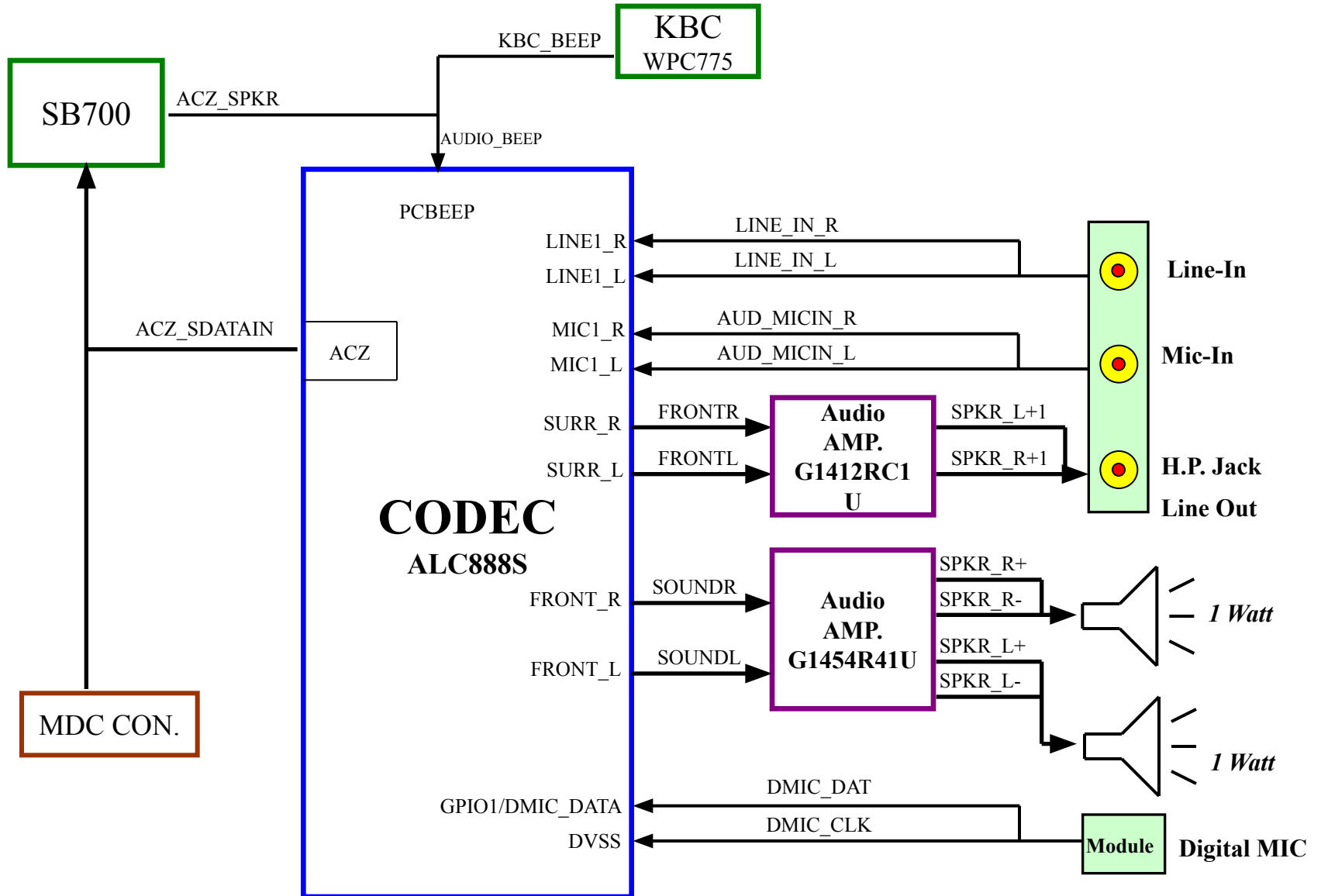
# JV50-PU POWER BLOCK DIAGRAM



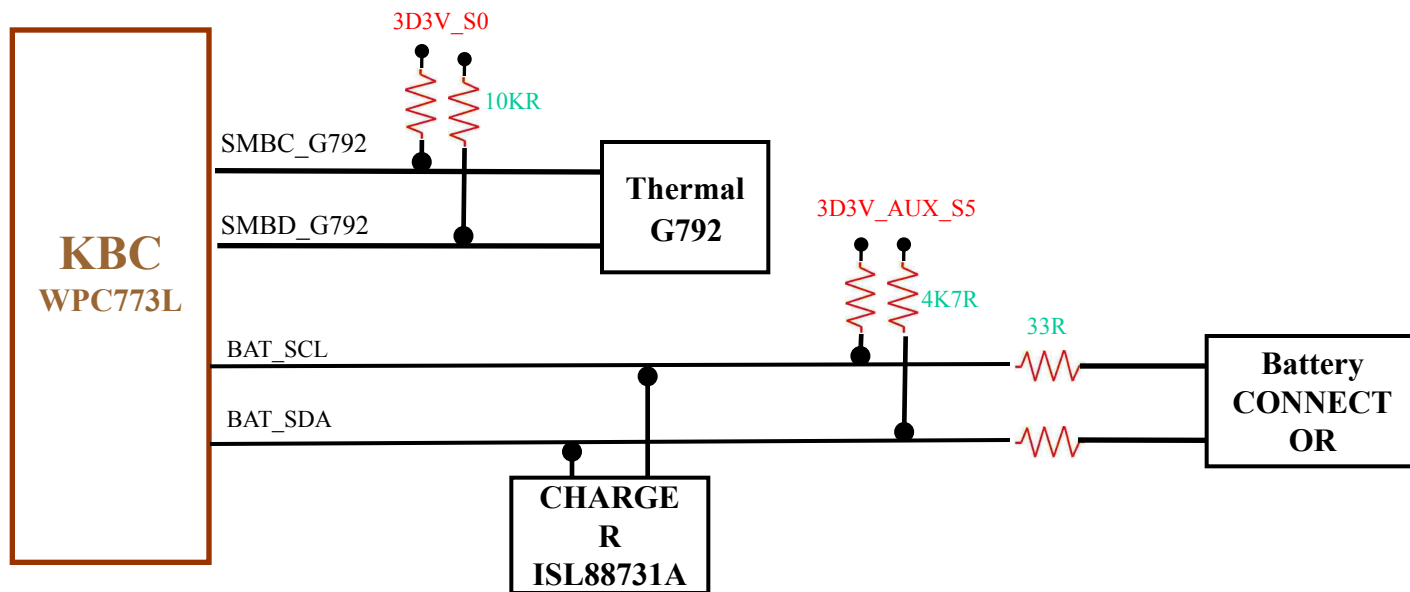
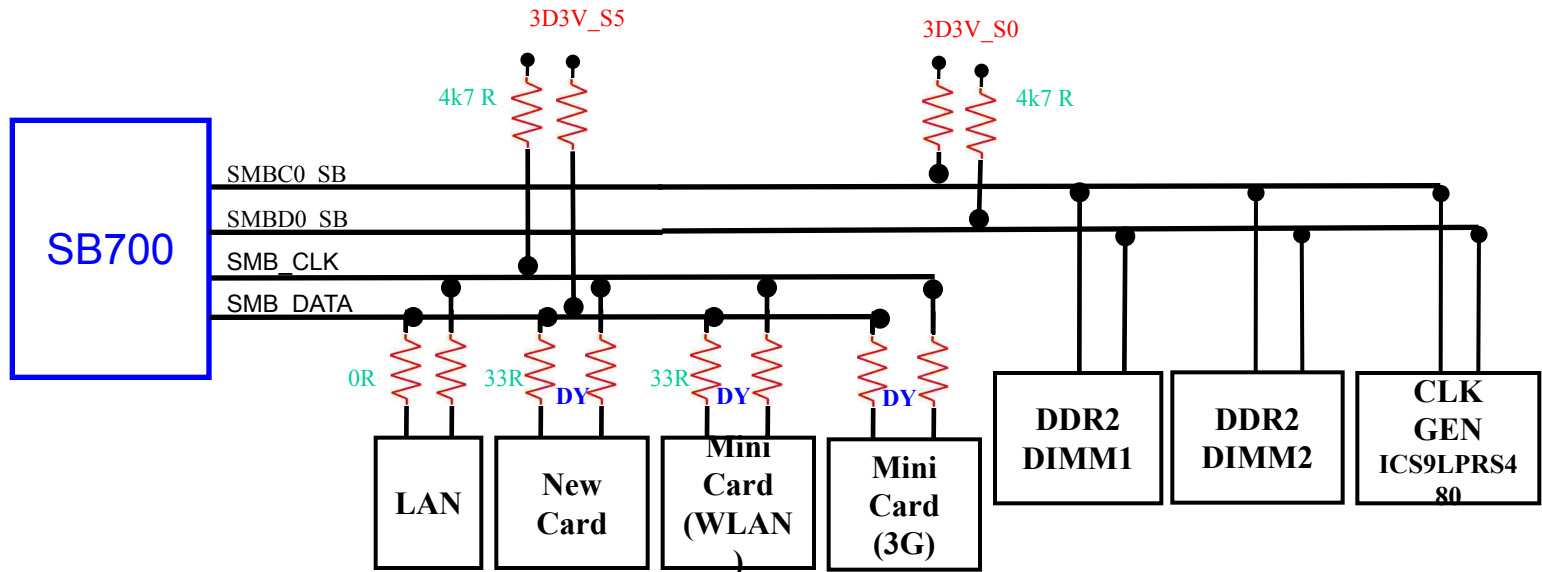
# JV50-PU Clock Block Diagram



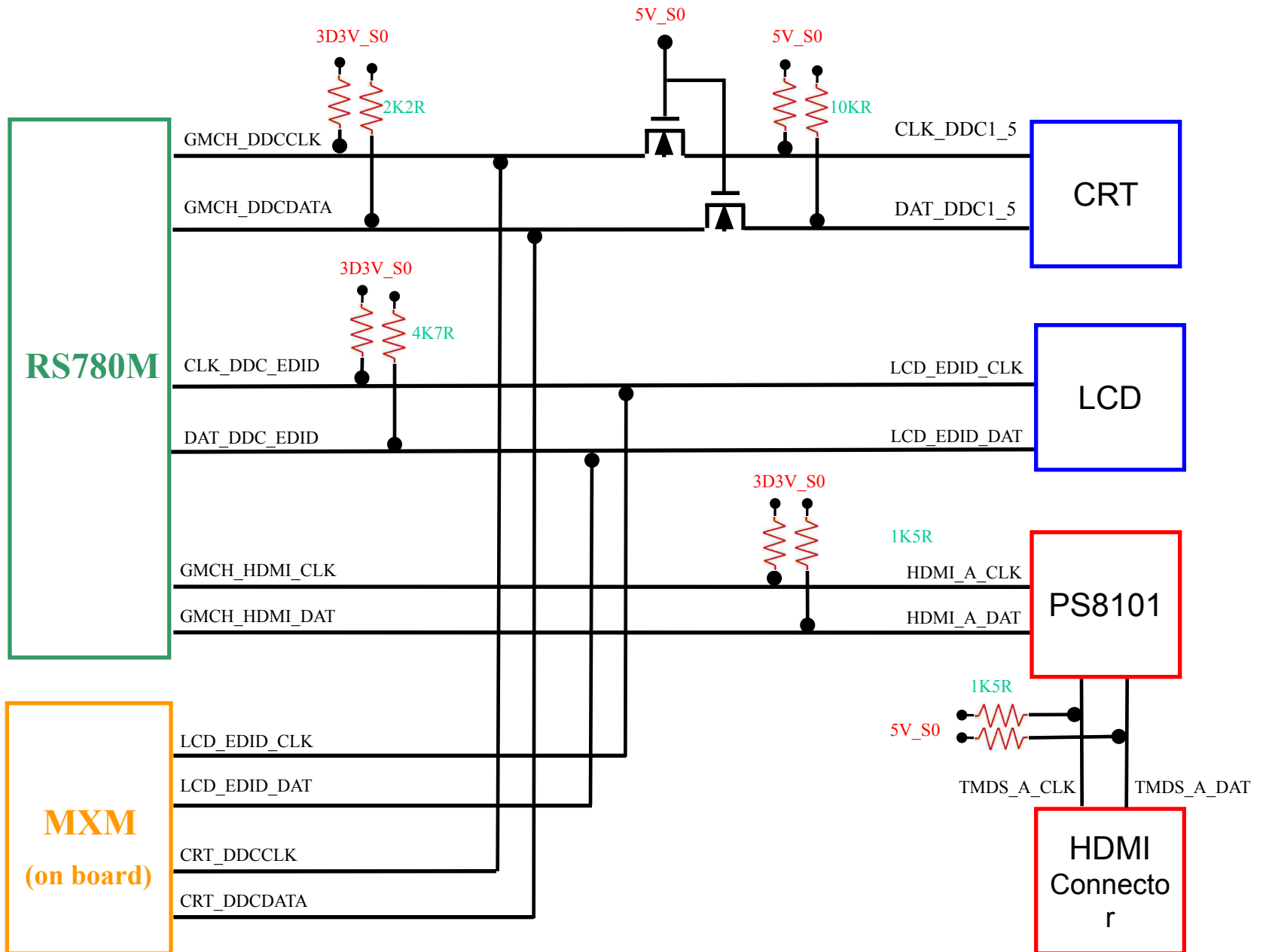
# JV50-PU Audio Block Diagram



# JV50-PU SMB Interface-1



# JV50-PU SMB Interface-2



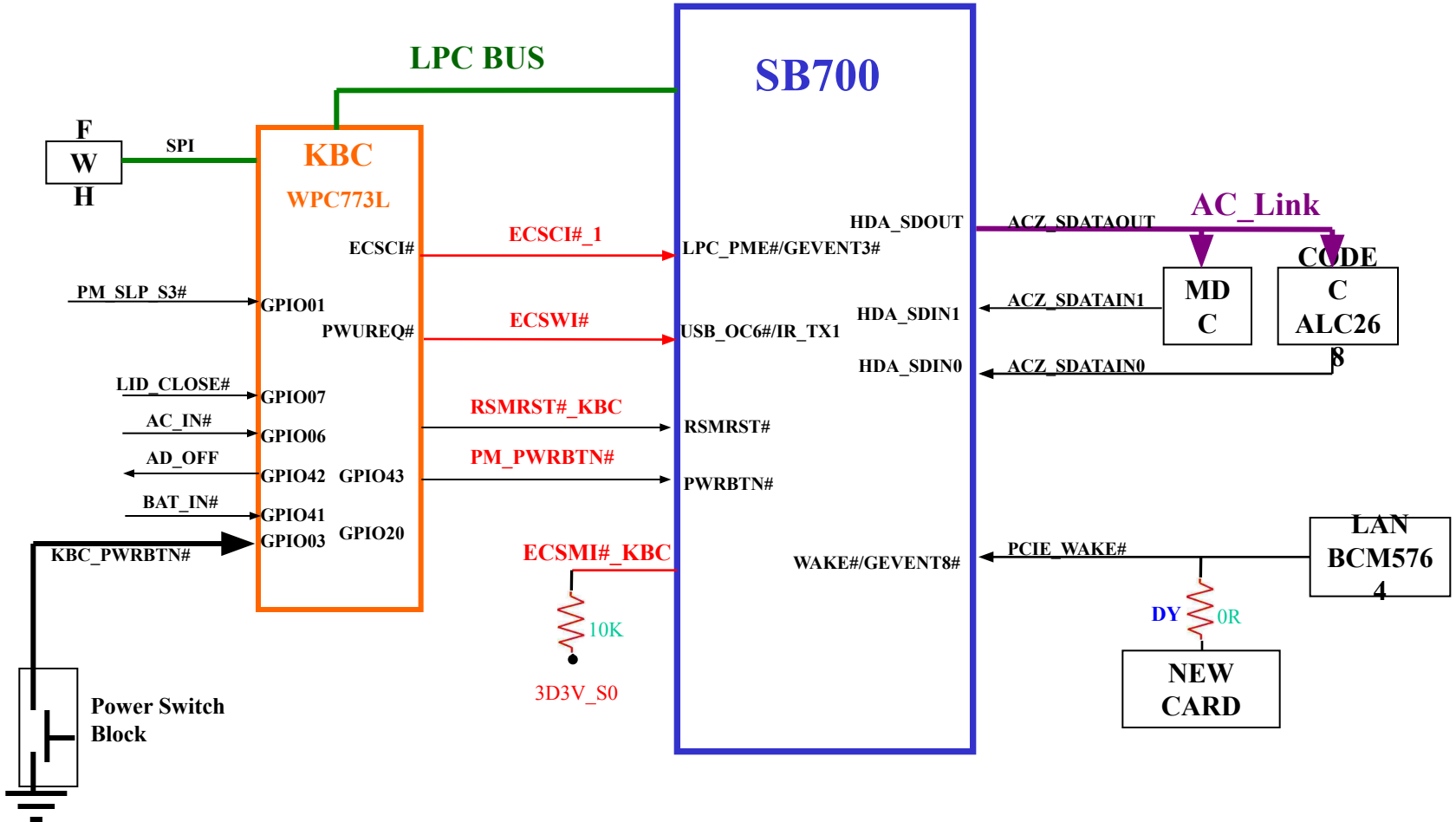


SMI在DOS底下動作

SCI在windows底下動作

SWI是指wake up event

# JV50-PU SMI/SCI/SWI Interface



From S3 state wakeup event: (1) Power Button; (2) WOL (AC Only); (3) Embedded Modem (AC Only); (4) RTC; (5) Lid; (6) Battery Critical

# JV50-PU VCC\_CORE Block Diagram

