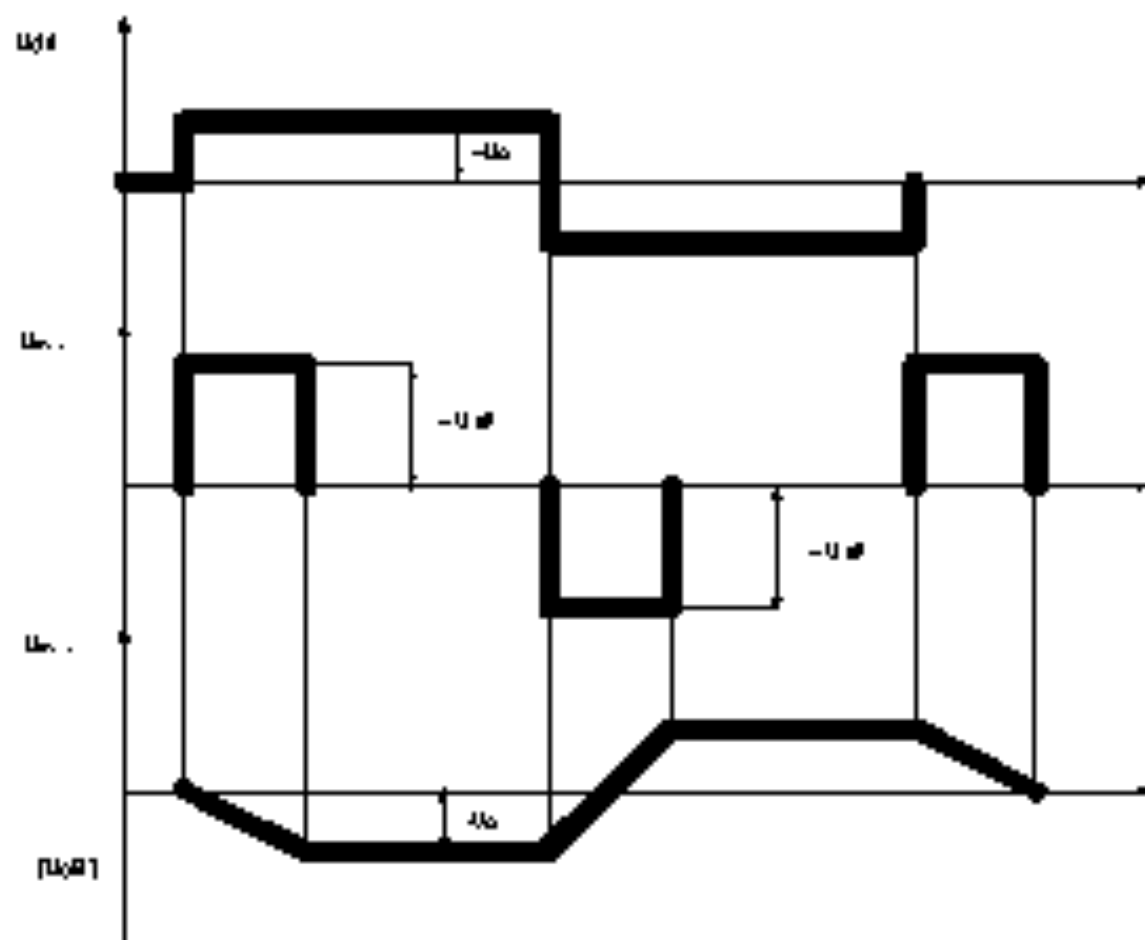
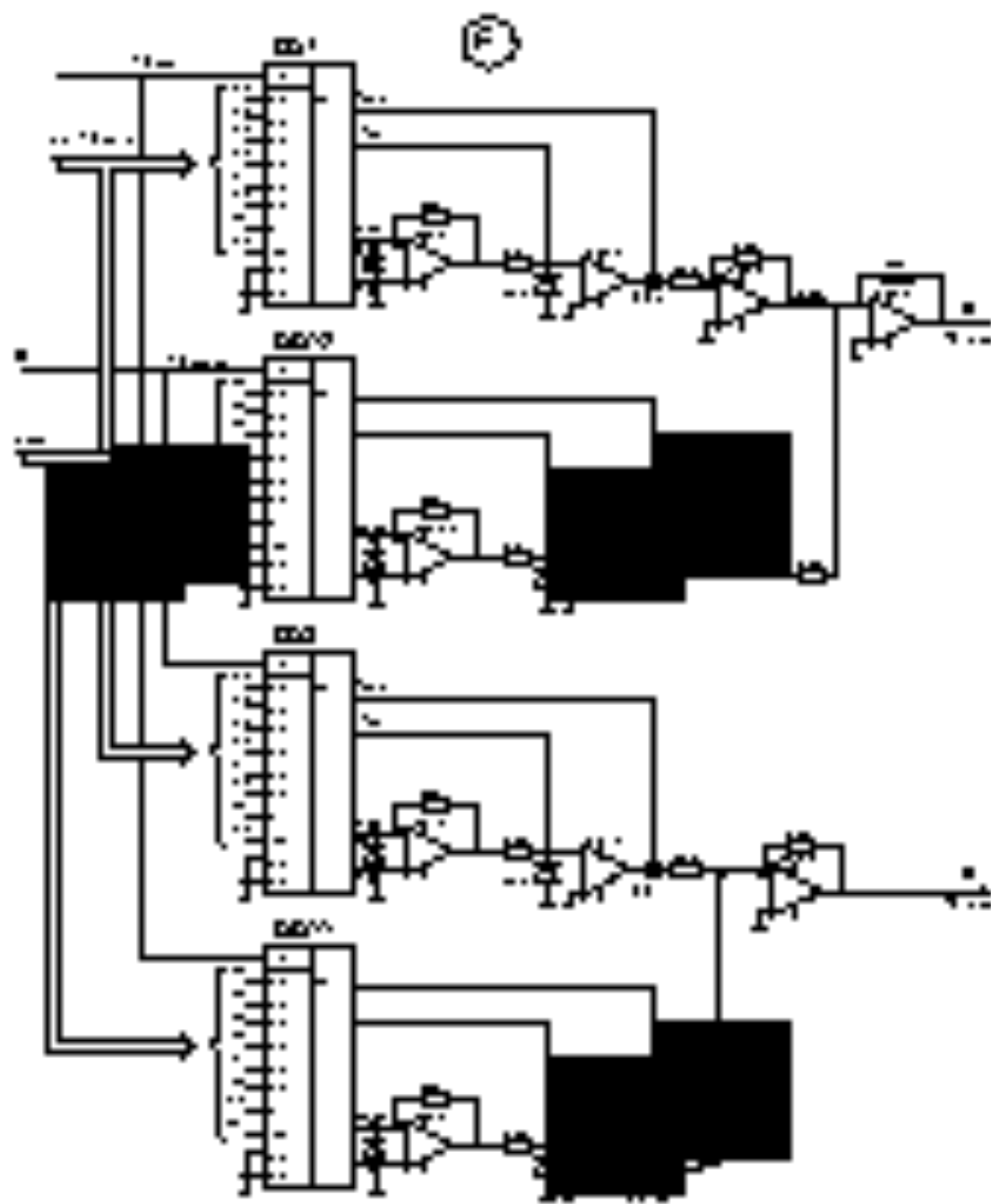


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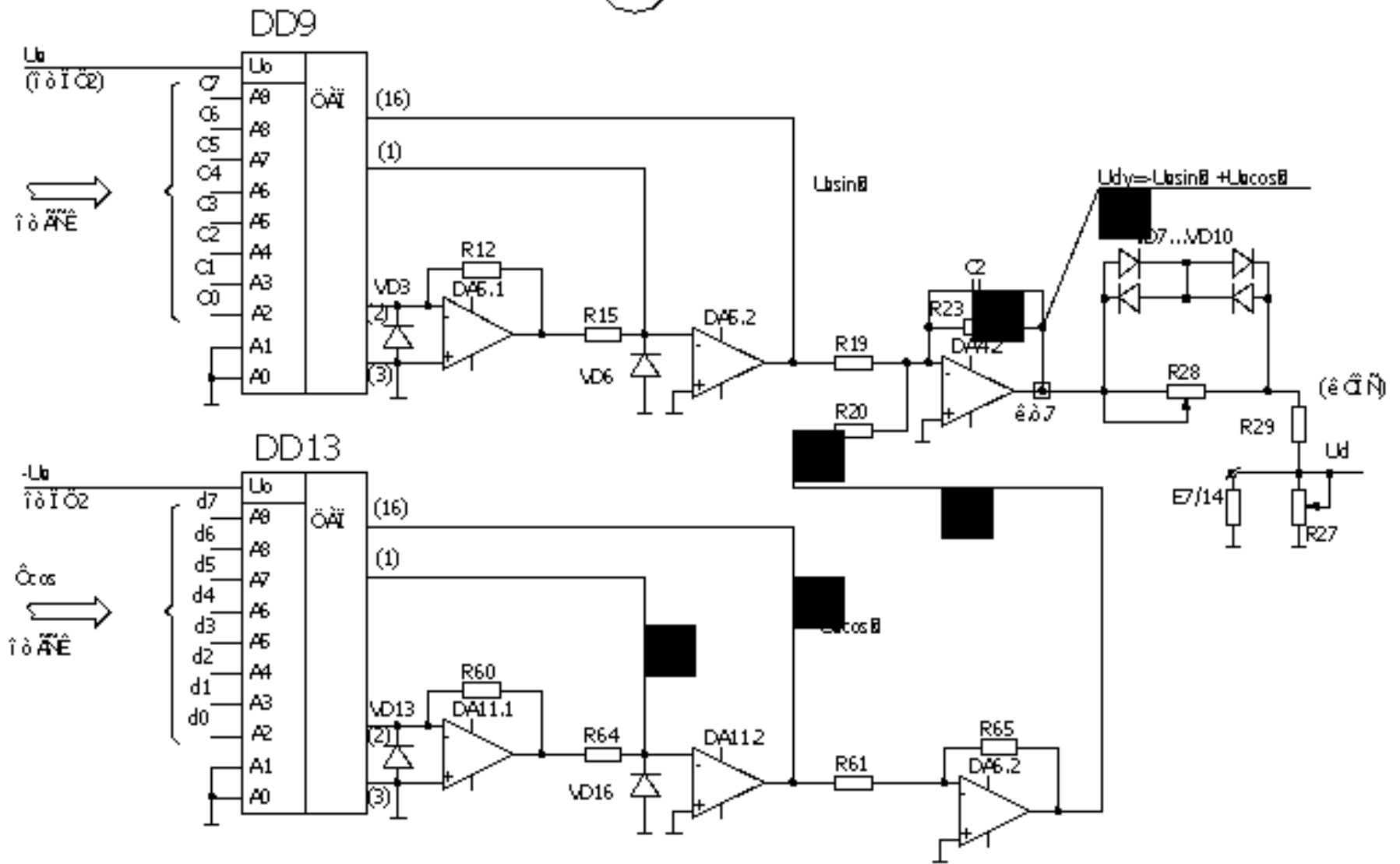


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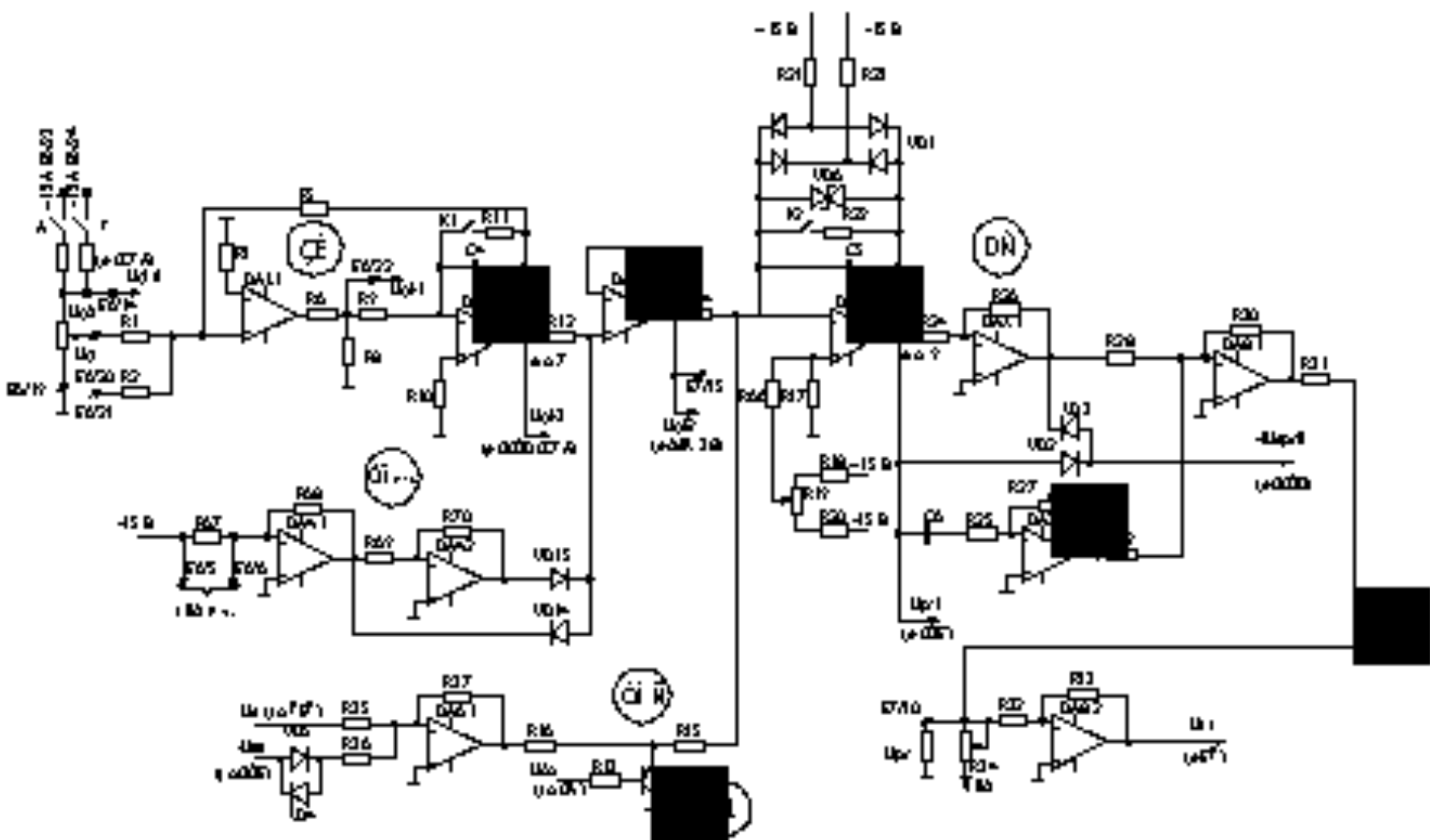


Bi rãnh môt số 1 đi đòng đòng.

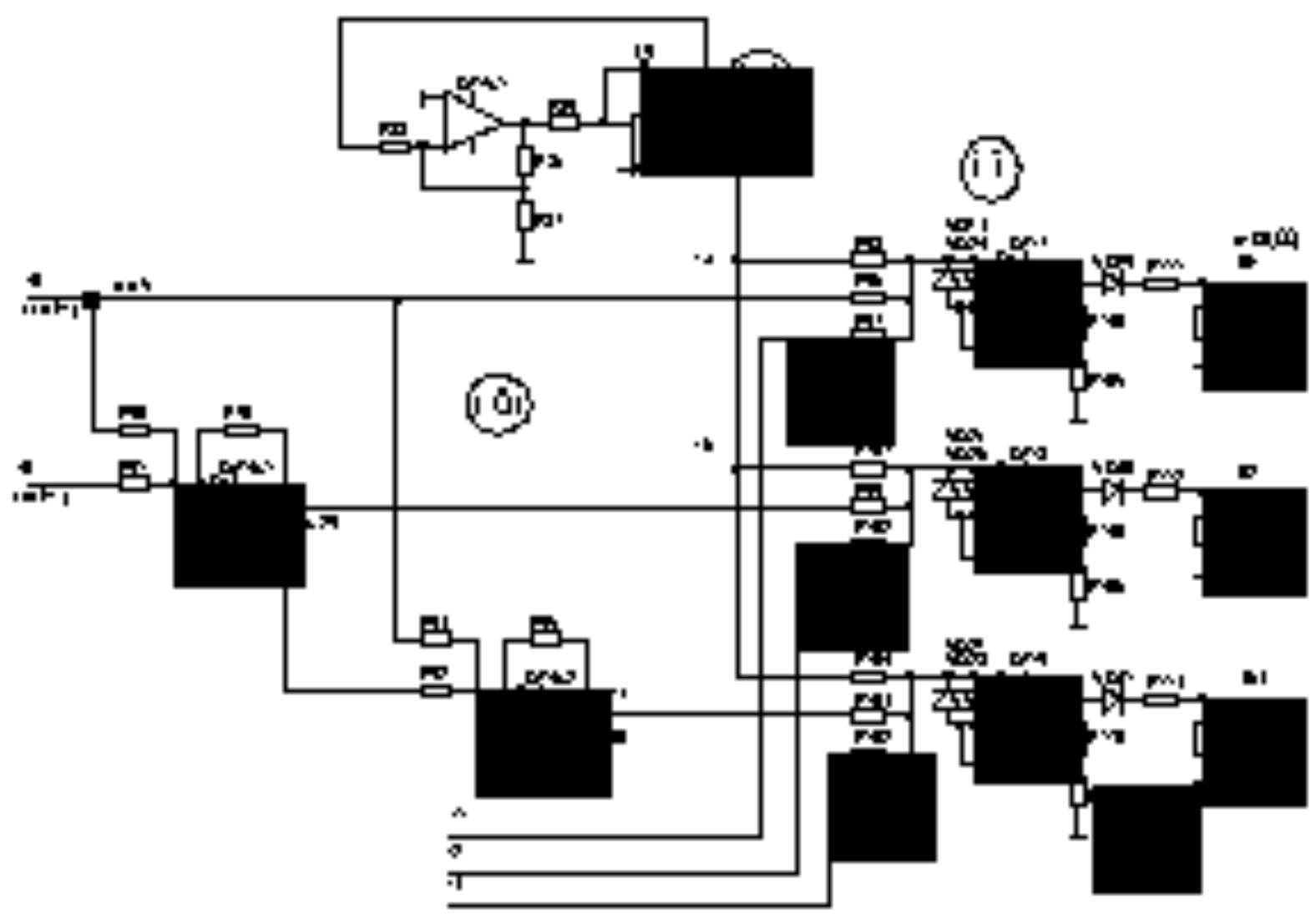
ÊË



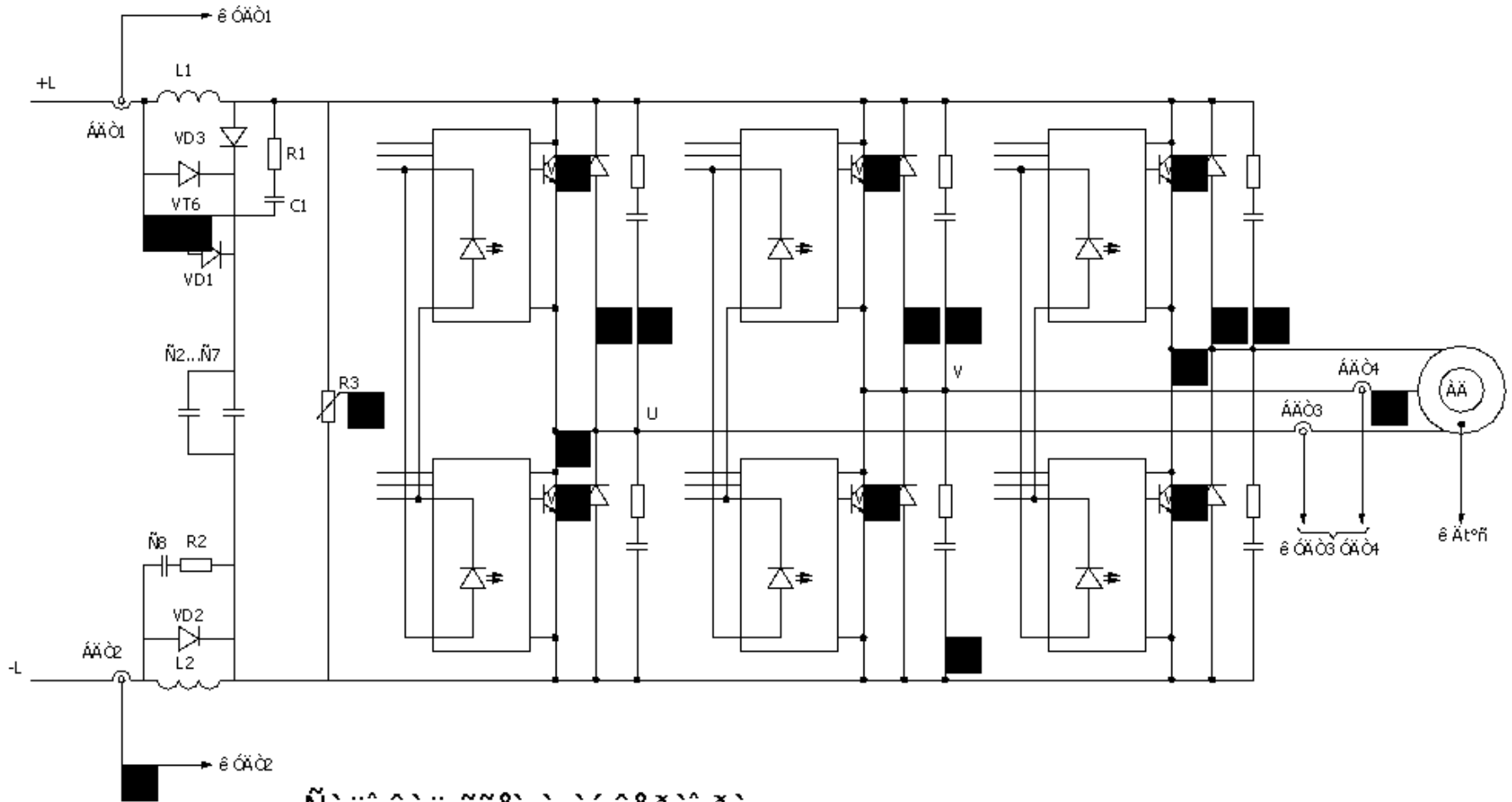
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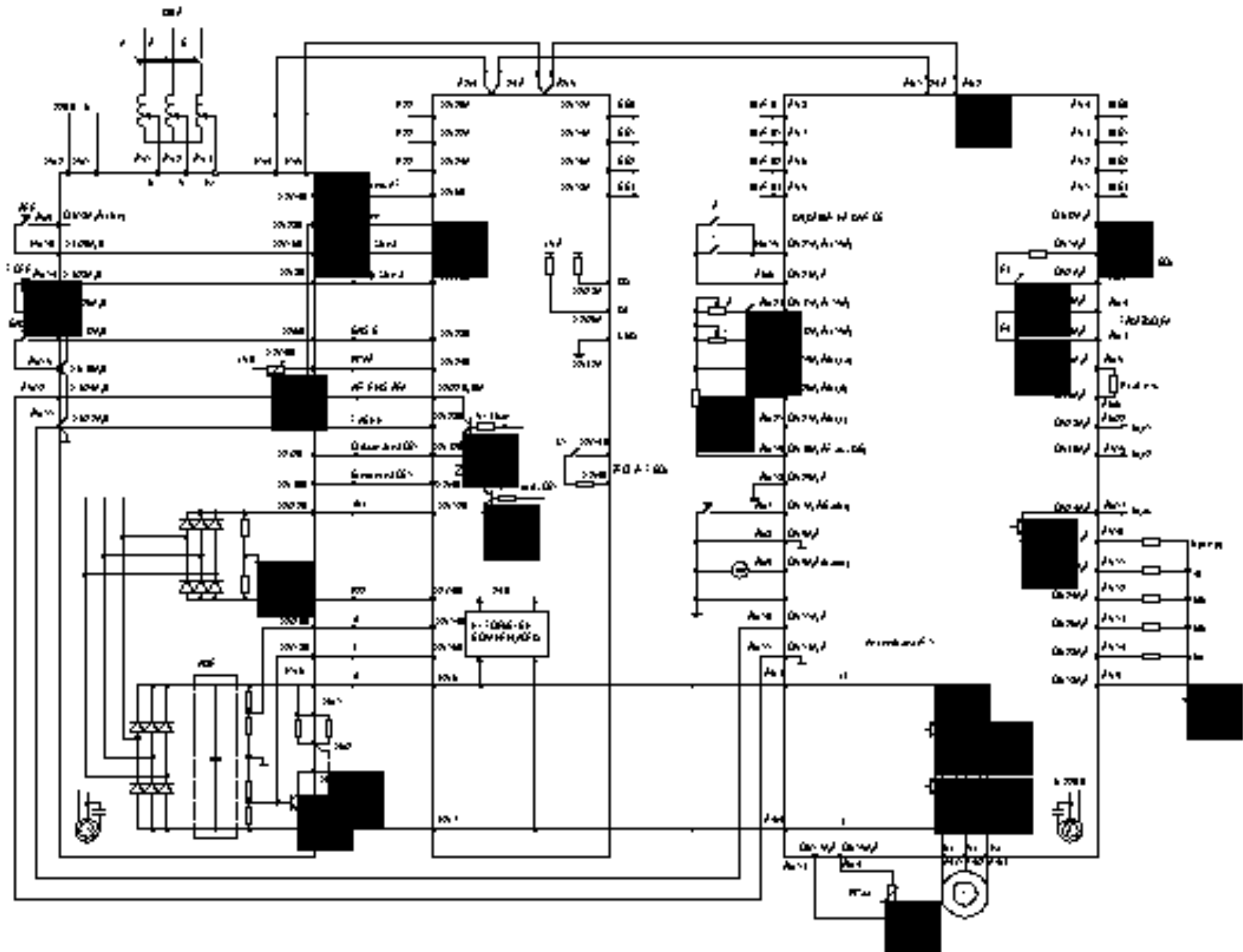
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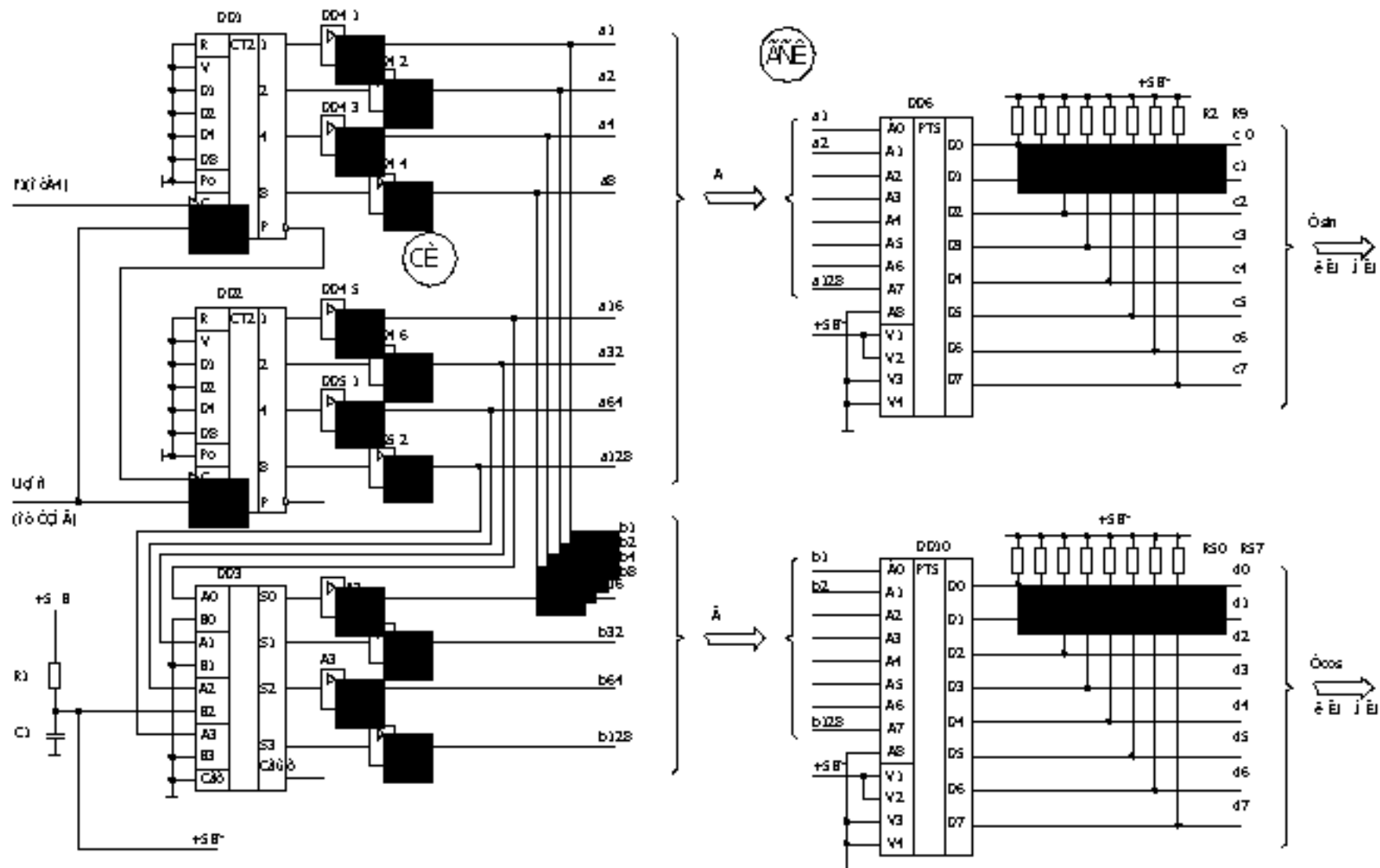
I 4-bit parallel adder circuit diagram. The carry-in circuit (ii) is used to generate the carry-in signal for the first full adder. The carry-out of each full adder is connected to the carry-in of the next full adder. The final sum and carry-out are shown as outputs of the 4-bit parallel adder block (i).



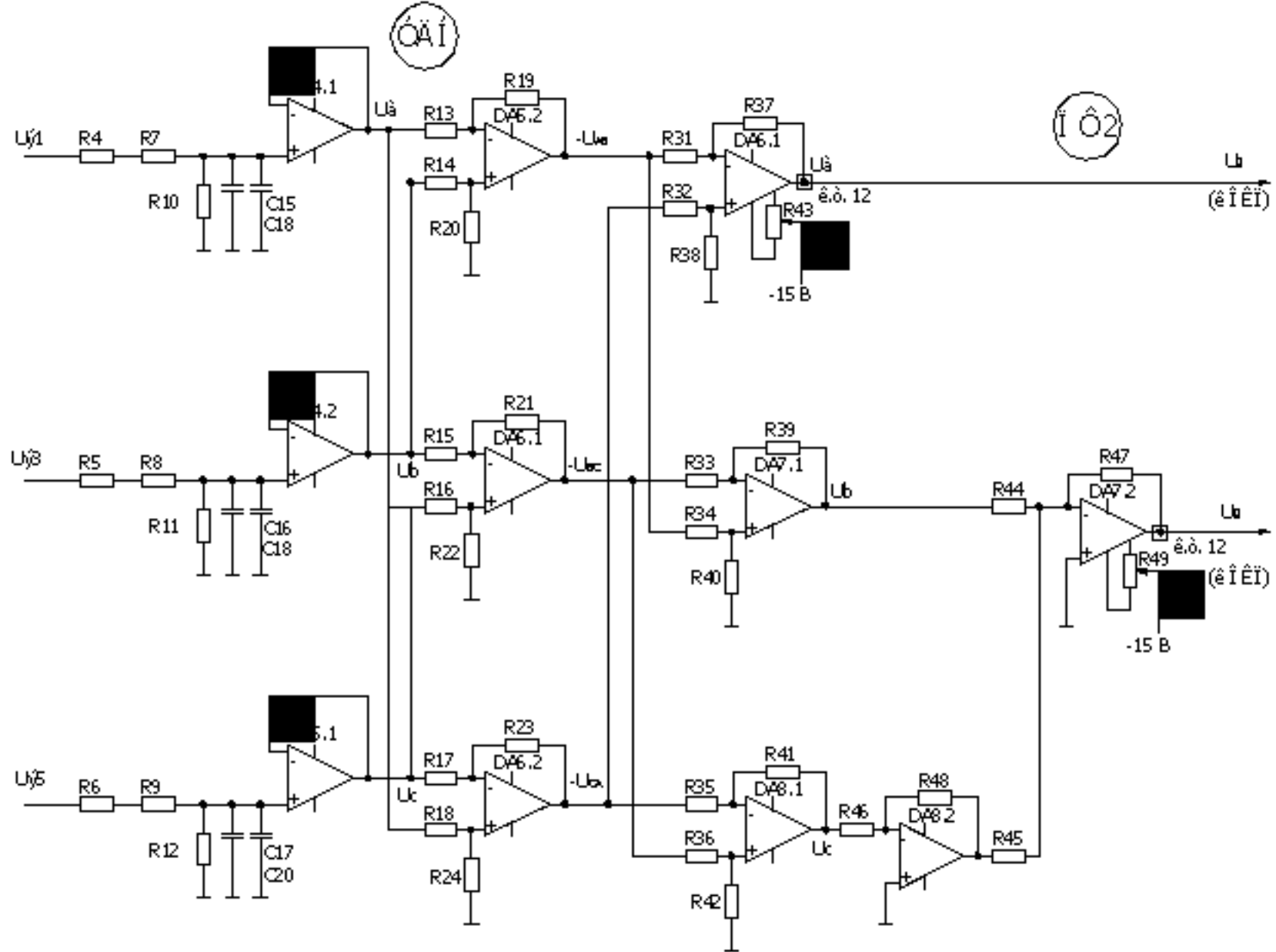
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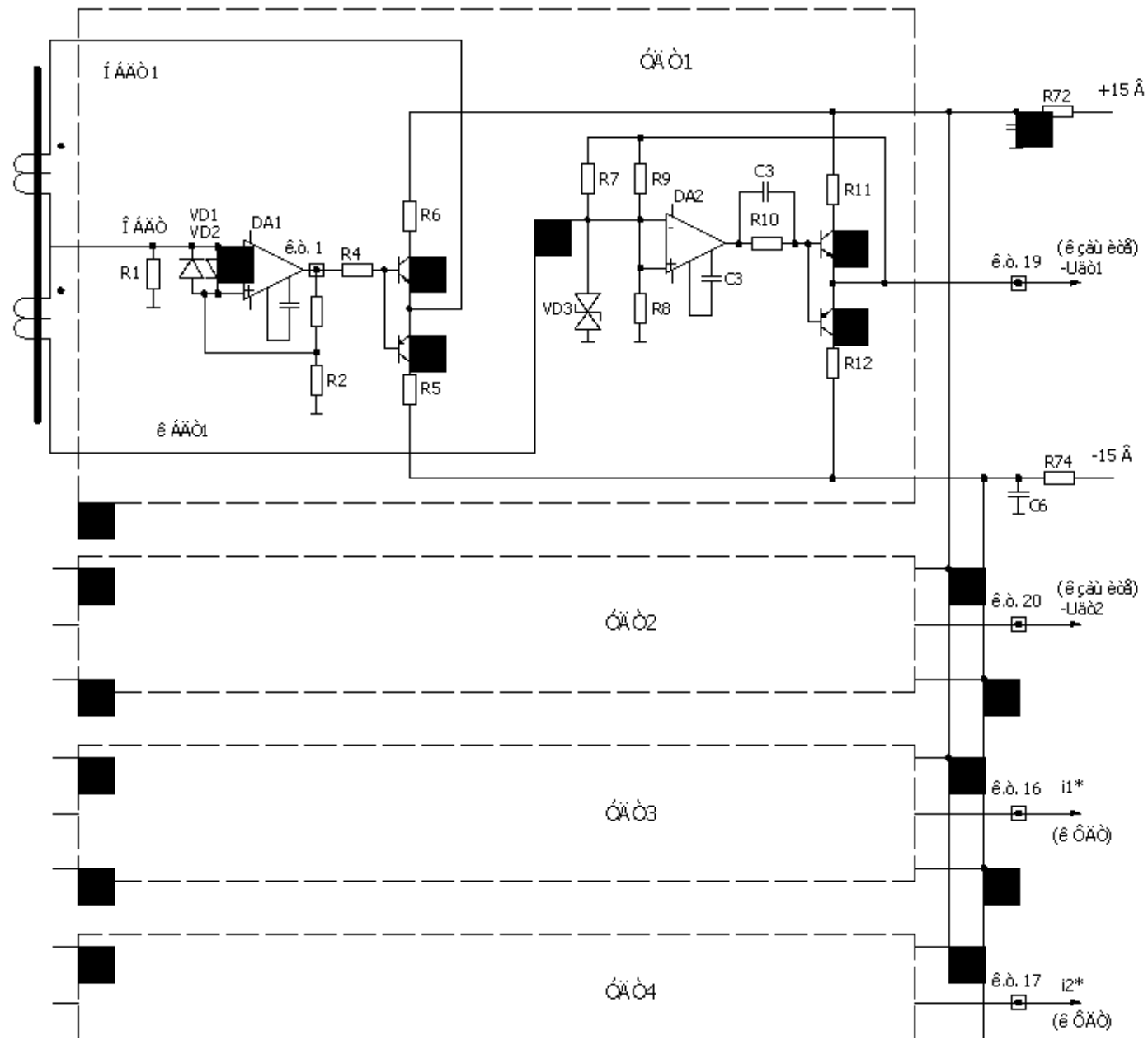
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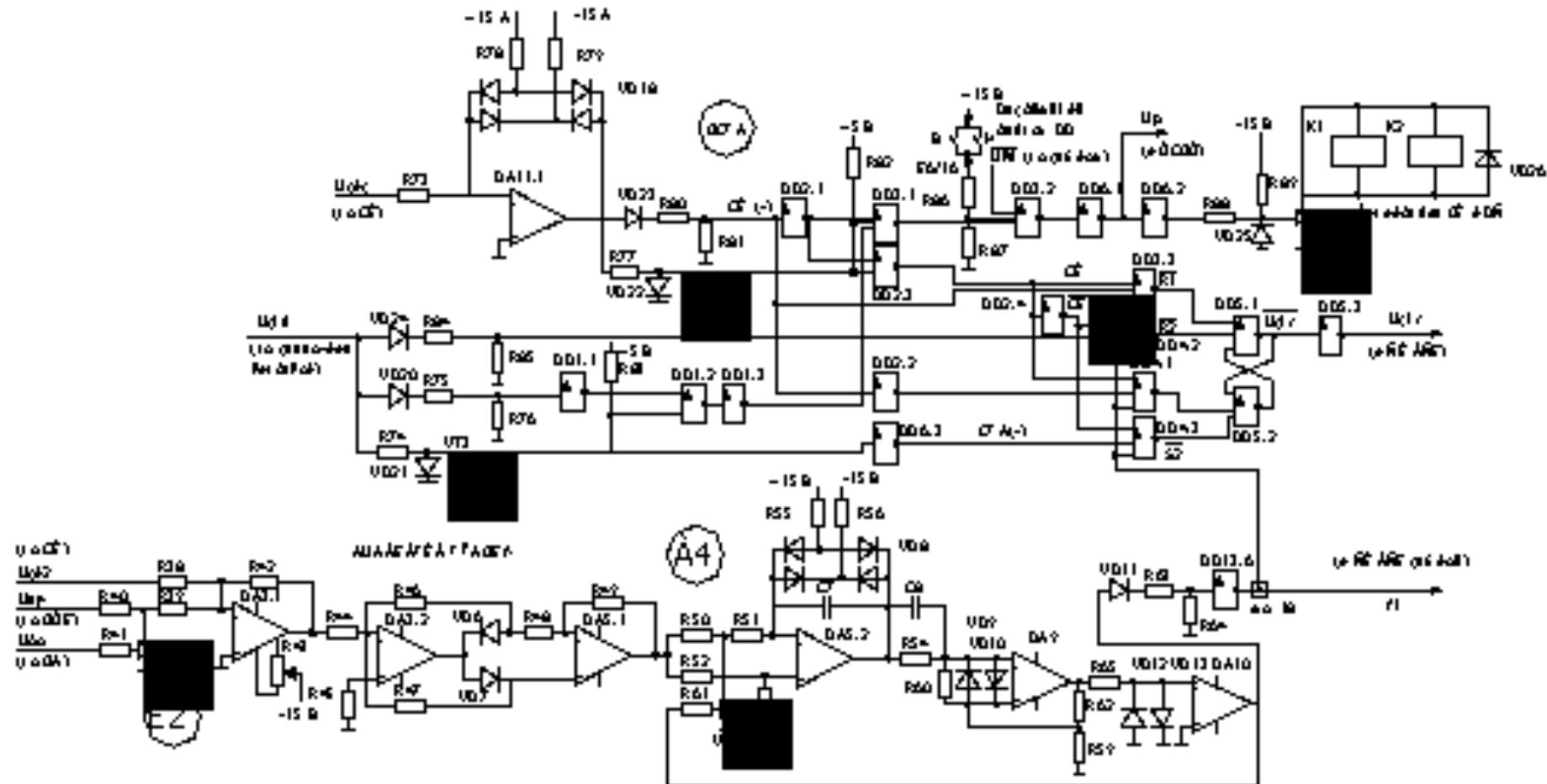


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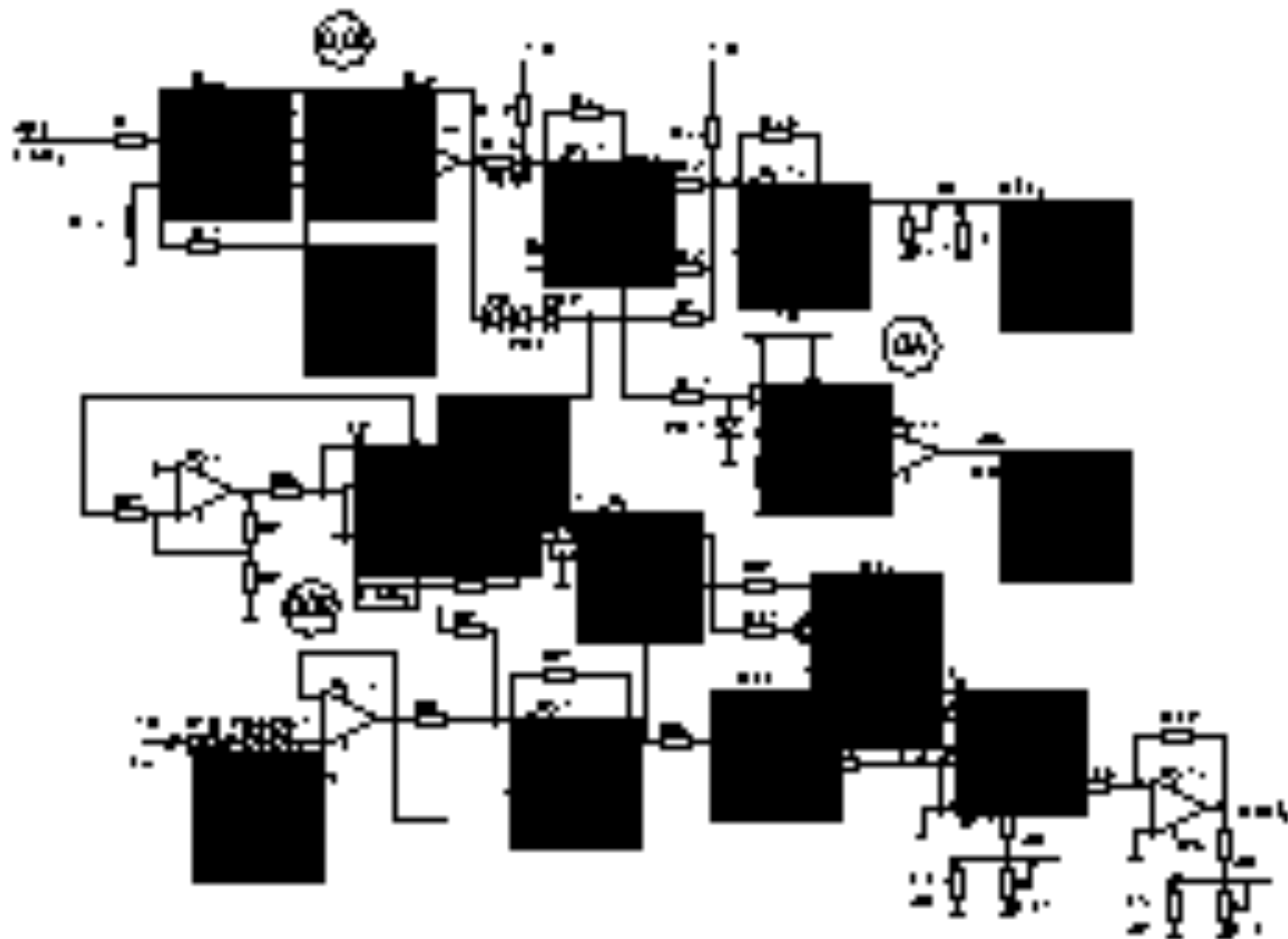


Óçæ äàò-èêî â í àî ðÿæáí èÿ, ï ðáî áòàçî äàòæü ó àç Ì Ô2.

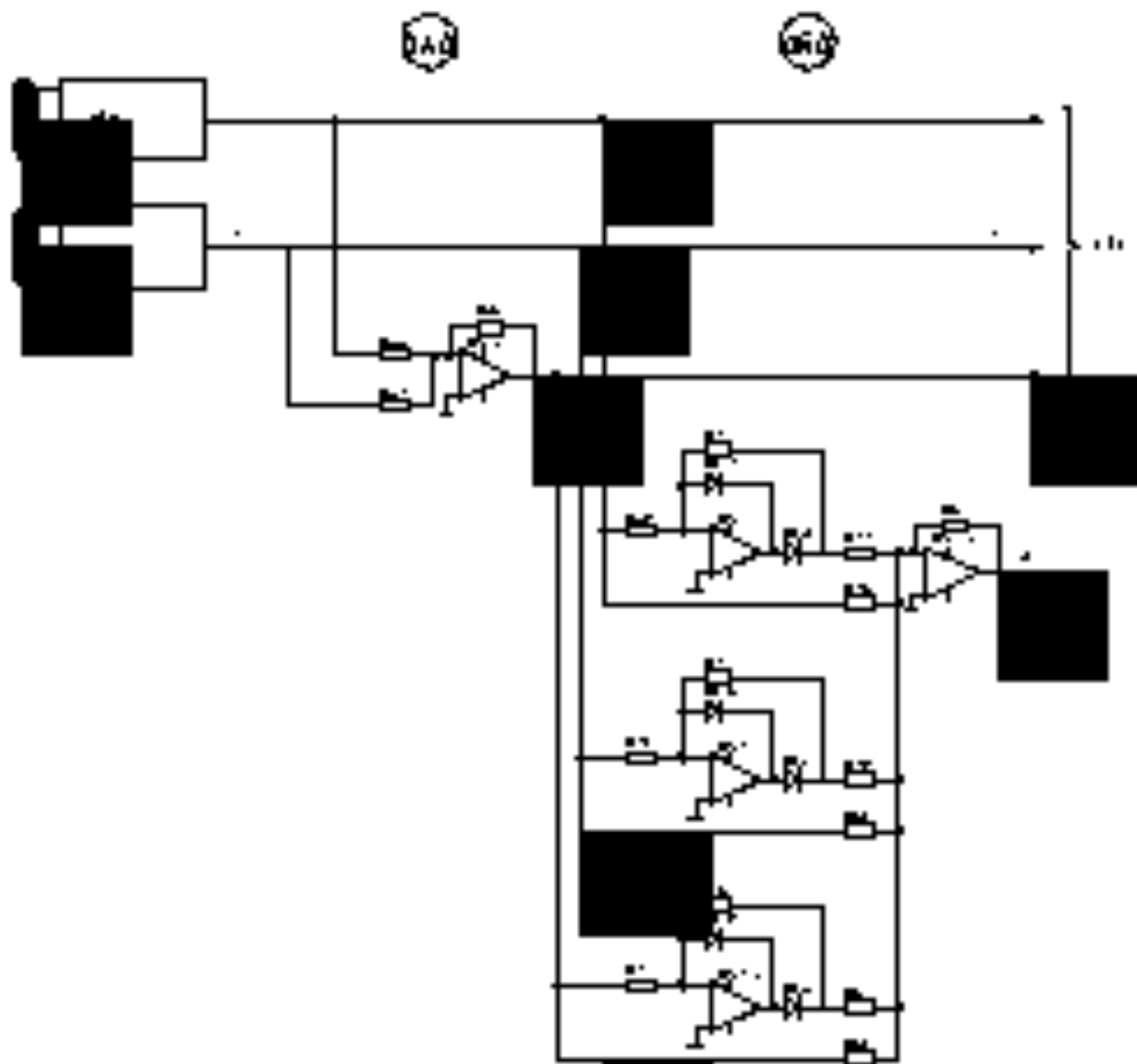




Çğæ çäääi èy í äi ääääâi èy äääü äi èy, çğæ "äi äei ä-ähüü ää"



ఈ పటం ఒక బహు-స్థాయి ఎలక్ట్రానిక్ సర్క్యూట్ యొక్క వివరాలను చూపిస్తుంది. సర్క్యూట్ యొక్క ప్రధాన భాగాలు, వాటి పనితీరు మరియు అనుబంధ భాగాలను వివరిస్తుంది.



Örni eðl. áæðleg áæðleg áæðleg, öðle röi i eðl. áæf ey ö'erið